

BAPATLA ENGINEERING COLLEGE:: BAPATLA
(Autonomous)
GBC ROAD, BAPATLA, ANDHRA PRADESH

A Two Day Workshop on "Custom IC Design"
Using Mentor EDA Tools
16th & 17th April, 2018

REGISTRATION FORM

Name :
Designation :
Organization :
Gender & Age :
Educational Qualification:
Address for
Communication
(with phone no.
& E-mail ID) :
Experience : Teaching __ years, Others__ years
Need Accommodation: YES/NO

DECLARATION

The above information is true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop. If selected, I shall attend the programme for the entire duration. I also undertake the responsibility to inform the Convener in case I am unable to attend the workshop.

Place:
Date:

Signature of the Applicant

National Workshop Organizing Committee

Chief Patrons

Sri M Seshagiri Rao, President, Bapatla Education Society
Sri M Nageswara Rao, Secretary, Bapatla Education Society

Patrons

Dr. N.Sudhakar, Principal, Bapatla Engineering College
Dr. B.Chandra Mohan, Professor & Head, ECE Dept.
Dr.K.Vizaya kumar,Academic Advisor ,BES

Convener

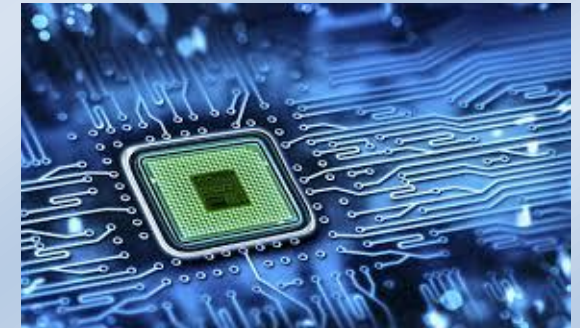
Smt, Y. Sruthi Suresh, Asst. Prof

Organizing committee

Sri Dr.N. Venkateswara Rao, Professor
Sri K. Sambasiva Rao, Assoc. Prof.
Sri P. Surendra Kumar, Asst. Prof.
Sri Ch. Altaff Hussian, Asst. Prof.
Sri M. Suneel, Assistant Professor
Smt. D. Swetha, Asst. Prof.
Sri Sk.M. Subhani, Asst. Prof.
Sri T. Krishna Chaitanya, Asst. Prof.
Sri B. Surendra Babu, Asst. Prof.
Sri Sk. Idrish, Asst. Prof.
Sri P.P. M Prasad, Asst. Prof.
Smt, N. Naga Swathi, Asst. Prof
Sri D. Suneel Varma, Asst. Prof
Sri K. Naga Raju, Asst. Prof
Smt, P. Tejaswini, Asst. Prof
Sri P. Vasudeva Reddy, Asst. Prof.
Sri S. Ramesh, Asst. Prof.
Sri P. Ravi, Asst.Prof.
Kum, K. Kalpana, Asst.Prof.
Sri Syed Imran Basha, Asst.Prof.
Sri G. Mahesh, Asst.Prof.
Sri Y.Ch.S Hareesh, Asst.Prof.
Sri N.V. Ramesh, Asst.Prof.
Sri B. Achyut Chowdary, Asst.Prof.
Sri D. Gopi Chand, Asst.Prof.
Sri U. Uma maheswara Rao, Asst.Prof.
Smt, Apsana Parveen , Asst.Prof.



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Department of Electronics and Communication Engineering
Bapatla Engineering College (Autonomous)
Mahatmaji Puram, GBC Road,
Bapatla, Guntur (Dt),
Andhra Pradesh, INDIA
Pin Code:-522102

Tel :08643-224244
Mobile :9030631990, 9985550585
Fax :08643-224246
E-mail :bececeworkshop18@gmail.com
Web :www.becbapatla.ac.in

In Association with



ABOUT THE COLLEGE

Bapatla Engineering College (Autonomous) was established by Bapatla Education Society in the year 1981 and is reckoned for its academic excellence in engineering & sciences. It is a constituent institution of Acharya Nagarjuna University and is one of the best private engineering institutions in the southern region. BEC offers graduate courses in 8 disciplines with total intake of above 1000 students, 5 post-graduate courses in CSE, CAD/CAM (Mech), PE(EEE), SE(CIVIL) and CESP(ECE). The College is a little away from the din and bustle of Bapatla, a town with a historic and hoary past, about 75 Km. south of Vijayawada on Chennai-Vijayawada rail route. All departments have well equipped laboratories in addition to the common facilities of workshops, central library, state-of-art central computing facility, sports facilities and innovation centers that houses research and testing facilities for industrial projects and technology labs to promote inter-disciplinary research activities.

ABOUT THE DEPARTMENT

The Department of Electronics & Communication Engineering is established in 1981 and have experienced, well-qualified, committed and motivated faculty with specializations in various streams. Currently ECE department offers a 4 year Graduate Program (B.Tech) in Electronics & Communication Engineering (ECE) and Post-Graduate (M.Tech.) program in Communication Engineering & Signal Processing (CESP), The Department was accredited thrice by NBA of AICTE and NAAC. The Department is also having IETE and ISTE Student Forums.

HOW TO REACH

The college is situated at Bapatla on the GBC (Guntur-Bapatla-Chirala) Road about 75 km from Vijayawada and 41 km from Guntur. It is well connected by air, road and rail.

PREAMBLE

VLSI design flow is the major key factor for present emerging systems and technology. The Workshop is dedicated in training Faculty & Students on VLSI Design. This training will give the participants an invaluable resource for those who are trying to enhance their knowledge about the current needs in the VLSI industry. This course is aimed and designed to provide a better exposure for faculty and students in the design and verification of digital circuits using front-end and back-end tools.

ABOUT CoreEL TECHNOLOGIES

CoreEL Technologies is a Customer Application Specific Product & Solutions (CASPS) company offering innovative solutions, which ranges across Intellectual Property (IP) cores, Design & Development, Bespoke System Design & Prototype Development, Next-Gen Digital Products, Integrated Solutions, Low Volume Manufacturing, System Upgrades and Obsolescence Management, EDA tools, COTS Products, Semiconductor Solutions and Technology Training. CoreEL is a leading developer of advanced electronic system level products and solutions to three primary markets - Aerospace & Defense, Digital Media Broadcast and Universities & Institutions of higher learning.

COURSE CONTENTS

- Basics of ASIC Design and ASIC Vs FPGAs
- Full Custom Flow and Semi Custom Design Flow
- Full Custom Design Flow
- Layout entry
- Semi Custom Design Flow
- Cell Based Design
- Xilinx Vivado by Targeting ZYNQ SOC (Zed Board)

RESOURCE PERSON

Mr.Nagendra Bandi

Application Engineer
CoreEL Technologies

SELECTION CRITERIA

Selection will be done based on *first-cum-first-serve basis* and the confirmed candidates will be notified immediately. The maximum number of participants will be **25 (twenty five)**.The selected participants will be notified through *e-mail*. Candidates will be issued certificates on successful completion of the course.

IMPORTANT DATES

Submission Deadline	10.04.2018
Intimation to Participants	12.04.2018
Confirmation by Participants	13.04.2018

REGISTRATION FEE

The Registration fee includes workshop kit, copy of the handouts, access to the lab, lunch during the workshop. A maximum of 25 participants are permissible on first come first serve basis and selected candidates will be intimated through email. The **Registration Fee Rs. 500/-** is to be transferred to the BEC special fee account bearing Acc no: **31118765060** and forward the receipt along with details of the participant to the mail id bececeworkshop18@gmail.com on or before **10/4/2018**.

ACCOMODATION

Accommodation is available on payment basis and limited hostel accommodation is available for Female participants.