

(54) Title of the invention : A NOVEL DESIGN OF SQUARE MICROSTRIP PATCH E-SLOTTED ANTENNA AT 28GHZ FOR 5G APPLICATION

<p>(51) International classification :H01Q0009040000, H01Q0001380000, H01Q0001220000, H01L0023660000, H01Q0021290000</p> <p>(86) International Application No :NA Filing Date :NA</p> <p>(87) International Publication No : NA</p> <p>(61) Patent of Addition to Application Number :NA Filing Date :NA</p> <p>(62) Divisional to Application Number :NA Filing Date :NA</p>	<p>(71)Name of Applicant :</p> <p>1)U Srinivasa Rao Address of Applicant :Department of ECE , Bapatla Engineering College Bapatla -----</p> <p>2)Kommalapati Rajesh 3)P Surendra Kumar 4)Miriya Suneel 5)Sadam Tharun Srinivas 6)Ventrpragada Gopi Krishna 7)Gudluri Lakshmi Swetha 8)Seelam Abhishek Reddy 9)Bapatla Engineering College Name of Applicant : NA Address of Applicant : NA</p> <p>(72)Name of Inventor :</p> <p>1)U Srinivasa Rao Address of Applicant :Department of ECE , Bapatla Engineering College Bapatla - -----</p> <p>2)Kommalapati Rajesh Address of Applicant :Assistant Professor, Department of Electronics and Communication Engineering, Bapatla Engineering College, Bapatla 522101, Andhra Pradesh, India Ongole -----</p> <p>3)P Surendra Kumar Address of Applicant :Associate Professor, Department of Electronics and Communication Engineering, Bapatla Engineering College, Bapatla 522101, Andhra Pradesh, India Bapatla -----</p> <p>4)Miriya Suneel Address of Applicant :Assistant Professor, Department of Electronics and Communication Engineering, Bapatla Engineering College, Bapatla 522101, Andhra Pradesh, India Chirala -----</p> <p>5)Sadam Tharun Srinivas Address of Applicant :Bapatla Engineering College, Bapatla Bapatla -----</p> <p>6)Ventrpragada Gopi Krishna Address of Applicant :Bapatla Engineering College, Bapatla Bapatla -----</p> <p>7)Gudluri Lakshmi Swetha Address of Applicant :Bapatla Engineering College, Bapatla Bapatla -----</p> <p>8)Seelam Abhishek Reddy Address of Applicant :Bapatla Engineering College, Bapatla Bapatla -----</p> <p>9)Bapatla Engineering College Address of Applicant :Bapatla Engineering College, Bapatla Bapatla -----</p>
---	---

(57) Abstract :

The research presented herein provides a comprehensive analysis of a square patch antenna featuring an E slot design tailored specifically for 5G wireless communication applications. This study meticulously explores the utilization of bandwidth in facilitating effective communication within the 5G spectrum. The suggested antenna design showcases the implementation of the microstrip feeding technique, a pivotal aspect of its operational framework. Within the scope of this investigation, an array of performance parameters including antenna bandwidth, return loss, gain, voltage standing wave ratio, efficiency, and directivity are meticulously evaluated and documented. The proposed antenna configuration is characterized by a dielectric constant of 2.2, strategically chosen to optimize its performance within the intended frequency range. Operating at a frequency of 28GHz, the antenna design is meticulously engineered utilizing an RT Duroid substrate 5880, distinguished by a height dimension of 1.5 mm. Noteworthy is the considerable bandwidth of 5.11 GHz exhibited by the proposed antenna, underscoring its efficiency in facilitating robust communication within the 5G domain. Through comparative analysis, the study highlights the superior performance of the proposed square patch antenna when juxtaposed against prevailing methodologies. Simulation activities integral to this study are conducted utilizing CST software, ensuring a rigorous evaluation and validation of the proposed design's effectiveness. By elucidating the comparative advantages and enhanced performance metrics of the proposed antenna configuration, this research endeavor contributes significantly to the advancement of 5G wireless communication technologies.

No. of Pages : 17 No. of Claims : 2