

BAPATLA ENGINEERING COLLEGE :: BAPATLA

(AUTONOMOUS)

Mahatmajipuram, Bapatla- 522 102

Department of Electronics and Communication Engineering

BEC/ECE/2023-24/I-Sem/

03-08-2023

TIME – TABLE
ACADEMIC YEAR - 2023-2024
II B.Tech, I Semester
SECTION 'A'

Room No: H 25

W.e.f: 28.08.2023

Day \ Hour	7.30AM to 8:20AM	8:20AM to 9:10AM	9:10AM to 10:00AM	B R E A K	10:30AM to 11:20AM	11:20AM to 12:10PM	12:10PM to 1:00PM	L U N C H B R E A K	2:00PM to 2:50PM	2:50PM to 3:40PM	3:40PM to 4:30PM
Mon	DS	P&S	DLD		DS/EDC LAB						
Tue	EDC	S&S`	EMFT		DLD	COI	P&S				
Wed	P&S	EDC	EMFT		DS	S&S	MENTORING (BCM)				
Thu	DS/EDC LAB				DLD	COI	EDC				
Fri	S&S	EMFT	DS		S&S LAB						
Sat	EMFT	DS	DLD		P&S	EDC	S&S				

20EC301	P&S	Probability and Statistics	Smt. K. Nagamani
20EC302	S&S	Signals and System	Mr. P. Sumanth Kumar
20EC303	EDC	Electronic Devices and Circuits	Dr. K. Rajesh
20EC304	EMFT	Electromagnetic Field Theory	Dr. B. Chandra Mohan
20EC305	DLD	Digital Logic Design	Dr. P. Vinodh Babu
20EC306	DS using Python	Data structures Using Python	Dr. Ch. V. M. S. N Pavan Kumar
20EC307	COI	Constitution of India	Sri. T. Siva Sankar
20ECL31	DS using Python Lab	Data Structures using Python Lab	1.Dr. Ch. V. M. S. N Pavan Kumar (MON,THU) 2.Smt. Y. Sruthi (THU) 3. Mr.Sk.M.Subhani (MON)
20ECL32	ED Lab	Electronic Devices Lab	1.Mr.Sk.Idrish (MON,THU) 2.Dr.P.Surendra Kumar (MON,THU)
20ECL33	S&S Lab	Signals & Systems Lab	1.Dr.U.Srinivasa Rao (FRI) 2.Mr.P.Sumanath Kumar (FRI)

HOD, ECE**PRINCIPAL**

CC: Principal, Dean–Academics, Examination Section, Dept. BECAP Coordinator, Notice Board.



BAPATLA ENGINEERING COLLEGE :: BAPATLA

(AUTONOMOUS)

Mahatmajipuram, Bapatla- 522 102

Department of Electronics and Communication Engineering

BEC/ECE/2023-24/I-Sem/

03-08-2023

TIME – TABLE
ACADEMIC YEAR - 2023-2024
II B.Tech, I Semester
SECTION 'B'

Room No: H 28

W.e.f: 28.08.2023

Day \ Hour	7.30AM to 8:20AM	8:20AM to 9:10AM	9:10AM to 10:00AM	B R E A K	10:30AM to 11:20AM	11:20AM to 12:10PM	12:10PM to 1:00PM	L U N C H B R E A K	2:00PM to 2:50PM	2:50PM to 3:40PM	3:40PM to 4:30PM
Mon	P&S	DS	EDC		S&S`	EMFT	MENTORING (MS)				
Tue	S&S LAB				P&S	DS	S&S`				
Wed	DS/EDC LAB				DLD	EDC	EMFT				
Thu	EMFT	P&S	DLD		EDC	COI	DS				
Fri	S&S`	DLD	DS		P&S	EDC	COI				
Sat	DLD	S&S`	EMFT		DS/EDC LAB						

20EC301	P&S	Probability and Statistics	Ms. K. Sunitha
20EC302	S&S	Signals and System	Dr.Ch. Nagaraju
20EC303	EDC	Electronic Devices and Circuits	Mr.Sk.Idrish
20EC304	EMFT	Electromagnetic Field Theory	Dr.J.Chandrasekhar
20EC305	DLD	Digital Logic Design	Mr. P. Madhu Kumar
20EC306	DS using Python	Data structures Using Python	Smt. Y. Sruthi
20EC307	COI	Constitution of India	Sri. K. Srinivasa Rao
20ECL31	DS using Python Lab	Data Structures using Python Lab	1.Smt. Y. Sruthi (WED,SAT) 2.Dr. Ch. V. M. S. N Pavan Kumar (WED) 3. Mr.G.Mahesh (SAT)
20ECL32	ED Lab	Electronic Devices Lab	1.Mr.Sk.Idrish (WED,SAT) 2.Dr.P.Surendra Kumar (WED,SAT)
20ECL33	S&S Lab	Signals & Systems Lab	1.Dr.U.Srinivasa Rao (TUE) 2.Dr. P. Vinodh Babu (TUE) 3. Dr. Ch. V. M. S. N Pavan Kumar (TUE)

HOD, ECE

PRINCIPAL

CC: Principal, Dean–Academics, Examination Section, Dept. BECAP Coordinator, Notice Board.



BAPATLA ENGINEERING COLLEGE :: BAPATLA

(AUTONOMOUS)

Mahatmajipuram, Bapatla- 522 102

Department of Electronics and Communication Engineering

BEC/ECE/2023-24/I-Sem/

03-08-2023

TIME – TABLE
ACADEMIC YEAR - 2023-2024
II B.Tech, I Semester
SECTION 'C'

Room No: H 22A

W.e.f: 28.08.2023

Day \ Hour	7.30AM to 8:20AM	8:20AM to 9:10AM	9:10AM to 10:00AM	B R E A K	10:30AM to 11:20AM	11:20AM to 12:10PM	12:10PM to 1:00PM	L U N C H B R E A K	2:00PM to 2:50PM	2:50PM to 3:40PM	3:40PM to 4:30PM
Mon	DS/EDC LAB				P&S	EDC	EMFT				
Tue	P&S	DLD	EDC		DS	EMFT	S&S				
Wed	EMFT	P&S	MENTORING (JCS)		COI	S&S	DLD				
Thu	EDC	DS	S&S		DS/EDC LAB						
Fri	DLD	S&S	DS		EDC	EMFT	COI				
Sat	S&S LAB				DS	P&S	DLD				

20EC301	P&S	Probability and Statistics	Mr. G. Sambasiva Rao
20EC302	S&S	Signals and System	Dr. U. Srinivasa Rao
20EC303	EDC	Electronic Devices and Circuits	Dr. K. Rajesh
20EC304	EMFT	Electromagnetic Field Theory	Dr. B. Chandra Mohan
20EC305	DLD	Digital Logic Design	Smt. Y. Sruthi
20EC306	DS using Python	Data structures Using Python	Mr.Sk.M.Subhani
20EC307	COI	Constitution of India	Mr. S. Vijay
20ECL31	DS using Python Lab	Data Structures using Python Lab	1.Mr.Sk.M.Subhani (MON,THU) 2.Mr.P.Madhu Kumar (THU) 3.Ms.K.Kalpana (MON)
20ECL32	ED Lab	Electronic Devices Lab	1.Dr. K. Rajesh (MON,THU) 2.Mr.K.Sambasiva Rao (MON) 3.Dr.U.Srinivasa Rao (THU)
20ECL33	S&S Lab	Signals & Systems Lab	1.Dr.U.Srinivasa Rao (SAT) 2.Smt.N.Nagaswathi (SAT) 3.Dr.P.Vinodh Babu (SAT)

HOD, ECE

PRINCIPAL

CC: Principal, Dean–Academics, Examination Section, Dept. BECAP Coordinator, Notice Board.