GATE - 1999

Electronics and Communication Engineering

Duration : Three hours

Maximum Marks : 150

SECTION A (75 Marks)

- This question consists of 25 (Twenty Five) multiple 1. choice questions, each carrying one mark. For each questions (1.1 - 1.25), four alternatives (a, b, c and d)are given, out of which only one is correct. $(25 \times 1 = 25)$
- 1.1 Identify which of the following is NOT a true of the graph shown in the given figure is



1.2 The *z*-transform F(z) of the function $f(nT) = a^{nT}$ is

(a)
$$\frac{z}{z-a^{\mathrm{T}}}$$
 (b) $\frac{z}{z+a^{\mathrm{T}}}$
(c) $\frac{z}{z-a^{\mathrm{T}}}$ (d) $\frac{z}{z+a^{-\mathrm{T}}}$

1.3 If [f(t)] = F(s), then [f(t - T)] is equal to (a) $e^{sT} F(s)$ (b) $e^{-sT} F(s)$

(c)
$$\frac{F(s)}{1 - e^{sT}}$$
 (d) $\frac{F(s)}{1 - e^{-sT}}$

A 2-port network is shown in the given figure. The 1.4 parameter h_{21} for this network can be given by



- 1.5 The early effect in a bipolar junction transistor is caused by
 - (a) fast turn-on
 - (b) fast turn-off
 - (c) large collector-base reverse bias
 - (d) large emitter-base forward bias

- The first dominant pole encountered in the 1.6 frequency response of a compensated op - amp is approximately at
 - (a) 5 Hz (b) 10 kHz
 - (c) 1 MHz (d) 100 MHz
- Negative feedback in an amplifier 1.7
 - (a) reduces gain
 - (b) increases frequency and phase distortions
 - (c) reduces bandwidth
 - (d) increases noise
- 1.8 In the cascade amplifier shown in the given figure, if the common - emitter stage (Q1) has a transconductance gm_1 , and the common base stage \mathbb{C}_{2}) has a transconductance g m_{2} , then the overall transconductance g (= i_0/v_i) of the cascade amplifier is

(a)
$$g_{m1}$$

(b) g_{m2} $v_{i} \sim --$

(c)
$$\frac{\delta m}{2}$$

(b)



- (d) $\frac{g_{m2}}{2}$
- Crossover distortion behaviour is characteristic of 1.9
 - (a) Class A output stage
 - (b) Class B output stage
 - (c) Class AB output stage
 - (d) Common base output stage
- **1.10** The logical expression $y = A + \overline{A} B$ is equivalent to

(a)
$$y = AB$$
 (b) $y = \overline{A}B$

- (c) $y = \overline{A} + B$ (d) y = A + B
- 1.11 A Darlington emitter follower circuit is somtimes used in the output stage of a TTL gate in order to
 - (a) increase its I
 - (b) reduce its I_{OH}
 - (c) increase its speed of operation
 - (d) reduce power dissipation

- 1.12 Commercially available ECL gears use two ground lines and one negative supply in order to
 - (a) reduce power dissipation
 - (b) increase fan- out
 - (c) reduce loading effect
 - (d) eliminate the effect of power line glitches or the biasing circuit
- 1.13 The resolution of a 4 bit counting ADC is 0.5 volts. For an analog input of 6.6 volts, the digital output of the ADC will be

(a)	1011	(b)	1101
(c)	1100	(<i>d</i>)	1110

1.14 For a second - order system with the closed - loop transfer function

$$T(s) = \frac{9}{s^2 + 4s + 9}$$

the settling time for 2 - percent band, in seconds, is

(<i>a</i>)	1.5	(b) 2.0
(c)	3.0	(d) 4.0

1.15 The gain margin (in *d*B) of a system ahaving the loop transfer function

G(s) H(s) =
$$\frac{\sqrt{2}}{s(s+1)}$$
 is
(a) 0 (b) 3
(c) 6 (d) ∞

1.16 The system moded described by the state equations is

$$X = \begin{bmatrix} 0 & 1 \\ 2 & -3 \end{bmatrix} x + \begin{bmatrix} 0 \\ 1 \end{bmatrix} u$$

 $Y = [1 \ 1] x$

- (a) controllable and observable
- (b) controllable, but not observable
- (c) observable, but not controllable
- (d) neither controllable nor observable
- 1.17 The phase margin (in degrees) of a system having the loop transfer function

G(s) H(s) =
$$\frac{2\sqrt{3}}{s(s+1)}$$
 is
(a) 45° (b) -30°
(c) 60° (d) 30°

- 1.18 A signal x(t) has a Fourier transform X(ω). If x (t) is a real and odd function of t, then X(ω) is
 - (a) a real and even function of ω
 - (b) a imaginary and odd function of ω
 - (c) an imaginary and even function of ω
 - (d) a real and odd function of ω
- 1.19 The input to a channel is a bandpass signal. It is obtained by linearly modulating a sinusoidal carrier with a single-tone signal. The output of the channel due to this input is given by

 $y(t) = (1/100) \cos (100t - 10^{-6}) \cos (10^{6}t - 1.56)$

The group delay (t_y) and the phase delay (t_p) in seconds, of the channel are

(a)
$$t_g = 10^{-6}, t_p = 1.56$$

- (b) $t_{s} = 1.56, t_{p} = 10^{-6}$
- (c) $t_g = 10^8, t_p = 1.56 \times 10^{-6}$
- (d) $t_g = 10^8, t_p = 1.56$

1.20 A modulated signal is given by,

 $s(t) = m_1(t) \cos(2\pi f_c t) + m_2(t) \sin(2\pi f_c t)$

where the baseband signal m_1 (t) and m_2 (t) have bandwidths of 10 kHz and 15 kHz, respectively. The bandwidth of the modulated signal, in kHz, is

(a)	10	(<i>b</i>)	15
(c)	25	(<i>d</i>)	30

1.21 A modulated signal is given by

 $s(t) = e^{-at} \cos \left[(\omega_v + \Delta \omega) t \right] u(t),$

where a, ω_e and $\Delta \omega$ are positive constants, and $\omega_e >> \Delta \omega$.

The complex envelope of s(t) is given by

- (a) $\exp(-at) \exp[j(\omega_{e} + \Delta \omega)t] u(t)$
- (b) $\exp(-at) \exp(j\Delta\omega t) u(t)$
- (c) $\exp(j\Delta\omega t)$. u(t)
- (d) $\exp[j\omega_{c} + \Delta\omega) t]$
- 1.22 An electric field on a plane is described by its potential

 $V = 20(r^{-1} + r^{-2})$

where *r* is the distance from the source. The field is due to

- (a) a monopole
- (b) a dipole
- (c) both a monopole and a dipole
- (d) a quadrupole

- 1.23 Assuming perfect conductors of a transmission line, pure TEM propagation is NOT possible in
 - (a) coaxial cable
 - (b) air-filled cylindrical waveguide
 - (c) parallel twin-wire line in air
 - (d) semi-infinite parallel plate wave guide
- 1.24 Indicate which one of the following will NOT exist in a rectangular resonant cavity.

(a)
$$TE_{110}$$
 (b) TE_{011}

(c)
$$TM_{110}$$
 (d) TM_{111}

1.25 Identify which one of the following will NOT satisfy the wave equation.

(a) $50 e^{j(\omega t - 3z)}$ (b) $\sin [\omega(10)]$	z + 50
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- (c) $\cos(y^2 + 5t)$ (d) $\sin(x) \cos(t)$
- This question consists of 25 (Twenty Five) multiple choice questions, each carrying two marks. For each question (2.1 2.25), four alternatives (*a*, *b*, *c* and *d*) are given, out of which only one is correct. (25 × 2 = 50)
- 2.1 The Fourier series representation of an impules train denoted by

$$s(t) = \sum_{n=-\infty}^{\infty} d(t - n T_0) \text{ is given by}$$

(a)
$$\frac{1}{T_0} \sum_{n=-\infty}^{\infty} \exp{-\frac{j2\pi nt}{T_0}}$$

(b)
$$\frac{1}{T_0} \sum_{n=-\infty}^{\infty} \exp{-\frac{j\pi nt}{T_0}}$$

(c)
$$\frac{1}{T_0} \sum_{n=-\infty}^{\infty} \exp \frac{j\pi nt}{T_0}$$

(d)
$$\frac{1}{T_0} \sum_{n=-\infty}^{\infty} \exp \frac{j2\pi nt}{T_0}$$

2.2 The Thevenin equivalent voltage V_{TH} appearing between the terminals A and B of the network shown in the given figure is given by



2.3 The value of R (in ohms) required for maximum power transfer in the network shown in the given figure



(c) 8 (d) 16

2.4 A Delta-connected network with its Wyeequivalent is shown in the given figure is . The resistances R₁/R₂ and R₃ (in ohms) are respectively



(a)	1.5, 3 and 9	(0)	3, 9 and 1.5
(c)	9, 3 and 1.5	(<i>d</i>)	3, 1.5 and 9

- 2.5 An *n*-channel JEFT has $I_{DSS} = 2mA$ and $V_p = -4 V$.Its transconductance gm (in milliohm) for an
applied gate-to-source voltage V_{GS} of -2 V is
(a) 0.25 (b) 0.5
(c) 0.75 (d) 1.0
- 2.6 An *npn* transistor (with C = 0.3 *p*F) has a unity gain cutoff frequency $f_{\rm T}$ of 400 MHz at a dc bias current $I_{\rm c} = 1$ mA. The value of its C_µ (in *p*F) is approximately (V_T = 26 mV)

- (c) 50 (d) 96
- 2.7 An amplifier has an open loop gain of 100, an input impedance of 1 kΩ, and an output impedance of 100 Ω. A feedback network with a feedback factor of 0.99 is connected to the amplifier in a voltage series feedback mode. The new input and output impedances, respectively, are
 - (a) 10Ω and 1Ω (b) 10Ω and $10 k\Omega$
 - (c) 100Ω and 1Ω (d) $100 k\Omega$ and $1k\Omega$
- 2.8 A dc power supply has a no-load voltage of 30 V, and a full-load voltage of 25 V at a full - load current of 1A. Its output resistance and load regulation, respectively, are
 - (a) 5Ω and 20% (b) 25Ω and 20%
 - (c) 5Ω and 16.7% (d) 25Ω and 10.7%

2.9 An amplifier is assumed to have a single - pole high-frequency transfer function. The rise time of its output response to a step function input is 35 nsec. The upper -3 dB frequency (in MHz) for the amplifier to a sinusoidal input is approximately at

(a)	4.55	(b) 10
(c)	20	(d) 28.6

2.10 The minimized form of the logical expression

$$(\overline{A} \ \overline{B} \ \overline{C} + \overline{A} B \overline{C} + \overline{A} B \overline{C} + \overline{A} B C + A B \overline{C})$$
 is

- (a) $\overline{A} \overline{C} + B\overline{C} + \overline{A}B$
- (b) $A\overline{C} + \overline{B}C + \overline{A}B$
- (c) $\overline{A}C + \overline{B}C + \overline{A}B$
- (d) $A\overline{C} + \overline{B}C + A\overline{B}$
- 2.11 For a binary half subtractor having two inputs A and B, the correct set of logical expressions for the outputs D (= A minus B) and X (= borrow) are
 - (a) $D = AB + \overline{A}B$, $X = \overline{A}B$
 - (b) $D = \overline{A}B + A\overline{B} + A\overline{B}$, $X = A\overline{B}$
 - (c) $D = \overline{A}B + A\overline{B}$, $X = \overline{A}B$
 - (d) $D = AB + \overline{A} \overline{B}$, $X = A\overline{B}$
- 2.12 The ripple counter shown in the given figure is works as a



- (*a*) mod 3 up counter
- (b) mod 5 up counter
- (c) mod 3 down counter
- (d) mod 5 down counter
- 2.13 If CS = A₁₅ A₁₄ A₁₃ is used as the chip select logic of a 4 K RAM in an 8085 system, then its memory range will be
 - (a) 3000 H 3 FFF H
 - (b) 7000 H 7 FFF H
 - (c) 5000 H 5 FFF H and 6000 H 6 FFF H
 - (d) 6000 H 6 FFF H and 7000 H 7 FFF H

2.14 If the closed - loop transfer function T(s) of a unity negative feedback system is given by

$$T(s) = \frac{a_{n-1}s + a_n}{s^n + a_1 s^{n-1} + \dots + a_{n-1}s + a_n}$$

then the steady state error for a unit ramp input is

(a)
$$\frac{a_n}{a_{n-1}}$$
 (b) $\frac{a_n}{a_{n-2}}$

(c)
$$\frac{a_{n-2}}{a_{n-2}}$$
 (d) zero

2.15 Consider the points $s_1 = -3 + j4$ and $s_2 = -3 - j2$ in the s-plane. Then, for a system with the open-loop transfer function

$$G(s) H(s) = \frac{K}{(s+1)^4}$$

- (a) s_1 is on the root locus, but not s_2
- (b) s_2 is on the root locus, but not s_1
- (c) both s_1 and s_2 are on the root locus
- (d) neither s_1 nor s_2 is on the root locus.
- 2.16 For the system described by the state equation

$$x = \begin{bmatrix} 0 & 1 & 0 \\ 0 & 0 & 1 \\ 0.5 & 1 & 2 \end{bmatrix} x + \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix} u$$

If the control signal *u* is given by u = [-0.5 - 3 - 5]*x* + *v*, then the eigenvalues of the closed-loop system will be

- 2.17 The z-transform of a signal is given by

$$C(z) = \frac{1z^{-1}(1-z^{-4})}{4(1-z^{-1})^2}$$

Its final value is

- (a) 1/4 (b) zero
- (c) 1.0 (d) infinity
- 2.18 The Nyquist sampling frequency (in Hz) of a signal given by

 $6 \times 10^4 \sin c^2 (400t)^* 10^6 \sin c^3 (100t)$ is

- (a) 200 (b) 300
- (c) 500 (d) 1000

2.19 The peak-to-peak input to an 8-bit PCM coder is 2 volts. The signal power-to-quantization noise power ratio (in *d*B) for an input of $0.5 \cos (\omega_m t)$ is

(a)	47.8	(b) 49.8
(c)	95.6	(d) 99.6

2.20 The input to a matched filter is given by

 $s(t) = \begin{cases} 10 \sin(2\pi \times 10^6 t) & 0 < 1 < 10^{-4} \sec(2\pi \times 10^{-6} t) \end{cases}$

otherwise

The peak amplitude of the filter output is

- (a) 10 volts (b) 5 volts
- (c) 10 millivolts (d) 5 millivolts
- 2.21 Four independent messages have bandwidths of 100 Hz, 200 Hz, and 400 Hz, respecively. Each is sampled at the Nyquist rate, and the samples are time division multiplexed (TDM) and transmitted. The transmitted sample rate (in Hz) is

(a)	1600	(b) 800

- (c) 400 (d) 200
- 2.22 In a twin-wire transmission line in air, the adjacent voltage maxima are at 12.5 cm and 27.5 cm. The operating frequency is

(a) 300 MHz	(b)	1 GHz
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<u>'</u> ;

(c) 2 GHz (d) 6.28 GHz

2.23 A trasmitting antenna radiates 251 W isotropically. A receiving antenna, located 100 m away from the transmitting antenna, has an effective aperture of 500 cm². The total received by the antenna is

(a)	10 µW	(b)	1 μW
(c)	20 µW	(<i>d</i>)	100 µW

- 2.24 In air, a lossless transmission line of length 50 cm with L = 10 μ H/m, C = 40 pF/m is operated at 25 MHz. Its electrical path length is
 - (a) 0.5 meters (b) λ meters
 - (c) $\pi/2$ radians (d) 180 degrees
- **2.25** A plane wave propagating through a medium $[\varepsilon_r = 8, v_r = 2, \text{ and } \sigma = 0]$ has its electric field given

by $\vec{E} = 0.5 \hat{\chi} e^{-(z/3)} \sin(10^8 t - \beta z) V/m.$

The wave impedance, in ohms is

- (a) 377 (b) $198.5 \angle 180^{\circ}$
- (c) $182.9 \angle 14^{\circ}$ (d) 133.3

SECTION B (75 Marks)

This section consists of TWENTY questions of FIVE marks each. ANY FIFTEEN out of them have to be answered. $(15 \times 5 = 75)$

EC 3. In the circuit of the the switch 'S' has remained open for a long time. The switch closes instantaneously at t = 0



- (a) Find V_0 for $t \le 0$ and as $t \to \infty$
- (b) Write an expression for V₀ as function of time for 0 ≤t ≤∞
- (c) Evaluate V_0 at $t = 25 \mu \text{sec.}$
- EC 4. For the network shown in the given figure is evaluate the current I flowing through the 2Ω resistor using superposition theorem.



- EC 5. A coil with a quality factor (Q) of 10 is put in series with a capacitor C_1 of 10 μ F, and the combination is found to draw maximum current when a sinusoidal voltage of frequency 50 Hz is applied. A second capacitor C_2 is now in parallel with the circuit. What should be the capacitance of C_2 for combined circuit to act purely as a resistance for a sinusoidal excitation at a frequency of 100 Hz? Calculate the rms current drawn by the combined circuit at 100 Hz if the applied voltage is 100 V (*rms*).
- **EC 6.** A bipolar junction transistor amplifier circuit is shown in the figure is Assume that the current source I_{bias} is ideal, and the transistor has vary large *b*, $r_b = 0$, and $r_0 \rightarrow \infty$.



Determine the *ac* small-signal midband voltage gain (V_0/V_s) , input resistance (R_i) , and output resistance (R_u) of the circuit. Assume $V_T = 26 \text{ mV}$

EC 7. A JFET having $\mu = 50$ and $r_d = 10$ KΩ is used in a common - source configuration as shown in the given figure. The JFET capacitances are $C_{gs} = 5pF$, $C_{gd} = 2 pF$, and $C_{ds} = 2 pF$. Determine the *ac* small-signal midband voltage gain (V_0/V_3) and the upper -3 *d*B frequency of the circuit.



EC 8. Neatly sketch and label the dc transfer characteristic (i.e., $V_0 vs. V_{in}$) of the circuit shown in the given figure as V_{in} varies from -2 V to +2 V. Assume ideal op-amp, and the diodes have a forward voltage of 0.6 V and zero incremental resistance.



EC 9. A transistor LC oscillator circuit is shown in the given figure. Assume that the transistor has very high β (so that you may neglect r_d). Derive an equation governing the circuit operation, and find the frequency of oscillation. Also, state the gain condition required for oscillation to start.



EC 10. In the CMOS inverter circuit shown in the figure is the input Vi makes a transition from V_{OL} (= 0 V) to V_{OH} (= 5V). Determine the high-tolow propagation delay time (t_{pHL}) when it is driving a capacitive load (C₁) of 20 pF. Device data :

NMOS:
$$V_{TN} = 1 V$$
; $k_N = \mu_n C_{OX} (W/L)_n$
= 40 $\mu A/V^2$, $\lambda = 0$,
PMOS: $V_{TP} = -1 V$, $k_p = \mu_p C_{OX} (W/L)_p$
= 20 $\mu A/V^2$, $\lambda = 0$.

Neglect body effect.



EC 11.The circuit diagram of a synchronous counter is shown in the given figure. Determine the sequence of states of the counter assuming that the initial state is '000'. Give your answer in a tabular form showing the present state $Q_{A(n)'}Q_{B(n)'}$ $Q_{C(n)'}$ J-K inputs ($J_{A'}$ K_{A'}, $J_{B'}$ J_{c'} K), and the next state $Q_{A(n+1)'}$ $Q_{B(n+1)'}$ $Q_{C(n+1)}$. From the table, determine the modulus of the counter.



EC 12. In a certain application, four inputs A, B, C, D (both true and complement forms available) are fed to logic circuit, producing an output F which operates a relay. The relay turns on when F (ABCD) = 1 for the following states of the inputs (ABCD): '0000', '0010', '0101', '0110', '1101' and '1110'. States '1000' and '1001' do not occur, and for the remaining states, the relay is off. Minimize F with the help of a Karnaugh map and realize it using a minimum number of 3 – input NAND gates. EC 13. An 8085 assembly language program is given below

	MVIC, 03H
	LXI H, 2000H
	MOV A, M
	DRC C
LOOP :	INX H
	MOV B, M,
	CMP B
	JNC LOOP2
	MOV A, B
LOOP2 :	DCR C
	JNZ LOOP1
	STA 2100H
	HLT

Contents of the memory locations 2000 H to 2002 H are

- 2000:18 H 2001:10 H, 2002: 2 BH.
- (a) What does the above program do?
- (b) At the end of the program, what will be
 - (*i*) the contents of the registers A, B, C, H and L?
 - (*ii*) the condition of the carry and zero flags ?(*iii*) the contents of the memory locations 2000 H, 2001 H, 2002 H, and 2100 H.
- **EC 14.**The loop transfer function of a feedback control system is given by

$$G(s)H(s) = \frac{K(s+1)}{s(1+T_s)(1+2s)}$$
, K > 0

Using Routh - Hurwitz criterion, determine the region of K - T plane in which the closed - loop system is stable.

EC 15. The asymptotic Bode plot of the minimum phase open-loop transfer function G(s) H(s) in as shown in the figure is Obtain the transfer function G(s) H(s)



EC 16. Consider a feedback system with the open-loop transfer function, given by

$$G(s)H(s) = \frac{K}{s(2s+1)}$$

Examine the stability of the closed-loop system using Nyquist stability theory.

EC 17. A baseband signal m(t) modulates a carrier to produce the angle modulated signal, AC cos $[2 \pi \times 10^8 t + k_p m(t)]$, where m(t) is shown in the figure is Determine the value of k_p so that the peak-to-peak frequency deviation of the carrier is 100 kHz.



- EC 18. Input to a linear delta modulator is a sinusoidal signal whose frequency can vary from 200 Hz to 4000 Hz. The input is sampled at eight times the Nyquist rate. The peak amplitude of the sinusoidal signal is 1 volt.
 - (a) Determine the minimum value of the step size in order to avoid slope overload when the input signal frequency is 800 Hz.
 - (b) What is the peak amplitude of the input signal, to just overload the modulator, when the input signal frequency is 200 Hz ?
 - (c) Is the modulator overlaoded when the input signal frequency is 4000 Hz ?
- EC 19. The power spectral density (PSD) of a noise process is given by

$$S_{N}(f) = \begin{cases} 10^{-8} \left(1 + \frac{|f| - 10^{8}}{10^{8}} \right) & f| < 10^{8} \\ 0 & f| > 10^{8} \end{cases}$$

The noise is passed through a unity-gain ideal bandpass filter, centered at 50 MHz and having a bandwidth of 2 MHz.

- (a) Sketch neatly the PSD of the output noise process.
- (b) Determine the output noise power.
- (c) Using the bandpass representation for the output noise process, sketch the PSD of the inphase and quadrature noise components, and determine their respective powers.

EC 20. A plane wave in free space with $\vec{E} = (\sqrt{\pi})$ (10.0

 $\hat{x} + 11.8 \ \hat{y}$). exp $j(4\pi \times 10^8 t - kz)]$,

where \hat{x} and \hat{y} are unit vectors in the x-y- directions, respectively, is incident normally on a semi-infinite block of ice as shown in the figure is

For ice, $\mu = \mu_{0'} \sigma = 0$, and $\varepsilon = 9\varepsilon_0 (1 - j0.001)$.



- (a) Calculate the average power density associated with the incident wave.
- (b) Calculate the skin depth in ice.
- (c) Estimate the average power density at a distance of 5 times the skine depth in the ice block, measured from the interface.
- EC 21.A 100 m section of an air-filled rectangular waveguide operating in the TE₁₀ mode has a crosssectional dimension of 1.071 cm \times 0.5 cm. Two pulses of 21 GHz and 28 GHz are simultaneously launched at one end of the waveguide section. What is the time delay difference between the two pulses at the other end of the waveguide ?
- EC 22. The average power of an omni directional antenna varies as the magnitude of $\cos \theta$, where θ is the azimuthal angle. Calculate the maximum Directive Gain of the antenna and the angles at which it occurs.

ANSWERS

1. 1 (c)	1. 2 (a)	1.3 (b)	1.4 (a)	1. 5 (c)	1.6 (<i>a</i>)	1.7 (a)	1.8 (<i>a</i>)	1.9 (b)	1. 10 (d)
1. 11 (c)	1. 12 (d)	1. 13 (b)	1. 14 (b)	1.15 (d)	1. 16 (d)	1. 17 (a)	1. 18 (a)	1. 19 (d)	1. 20 (c)
1. 21 (c)	1. 22 (c)	1. 23 (d)	1. 24 (a)	1. 25 (c)	2.1 (a)	2. 2 (d)	2.3 (*)	2. 4 (d)	2. 5 (b)
2.6 (d)	2. 7 (c)	2. 8 (<i>d</i>)	2. 9 (d)	2. 10 (<i>a</i>)	2. 11 (c)	2. 12 (d)	2. 13 (<i>d</i>)	2. 14 (<i>d</i>)	2. 15 (d)
2. 16 (d)	2. 17 (b)	2. 18 (<i>d</i>)	2. 19 (c)	2. 20 (a)	2. 21 (b)	2. 22 (b)	2. 23 (d)	2. 24 (a)	2. 25 (a)