Bapatla Engineering College:: Bapatla
(Autonomous)

SCHEME OF INSTRUCTION & EXAMINATION (Semester System)
For
Electronics and Communications Engineering
Effective from the Academic Year 2018-2019 (R18 Regulations)
First Year B.Tech (SEMESTER – I)

Code No.	Subject	Scheme of Instruction (Periods per week)				Scheme of Examination (Maximum marks)			No. of Credits
		L	T	P	Total	CIE	SEE	Total Marks	
18MA001	Linear Algebra and ODE	4	0	0	4	50	50	100	3
18PH001	Waves and Modern Physics	4	0	0	4	50	50	100	3
18CY001	Engineering Chemistry	4	0	0	4	50	50	100	3
18CE001	Environmental Studies	3	0	0	3	50	50	100	2
18CS001	Problem Solving with Programming	4	0	0	4	50	50	100	3
18CYL01	Engineering Chemistry Lab	0	0	3	3	50	50	100	1
18ECL12	Hardware Lab	0	0	3	3	50	50	100	1
18CSL01	Problem Solving with Programming Lab	0	0	3	3	50	50	100	1
	TOTAL	19	0	9	28	400	400	800	17

CIE: Continuous Internal Evaluation   SEE: Semester End Examination
L: Lecture,   T: Tutorial,   P: Practical

SCHEME OF INSTRUCTION & EXAMINATION (Semester System)
For
Electronics and Communications Engineering
Effective from the Academic Year 2018-2019 (R18 Regulations)
First Year B.Tech (SEMESTER – II)

Code No.	Subject	Scheme of Instruction (Periods per week)				Scheme of Examination (Maximum marks)			No. of Credits
		L	T	P	Total	CIE	SEE	Total Marks	
18MA002	Numerical Methods and Advanced Calculus	4	0	0	4	50	50	100	3
18EC202	Basic Instrumentation	4	0	0	4	50	50	100	3
18EC203	Programming with C ++	4	0	0	4	50	50	100	3
18EL001	Communicative English	3	0	0	3	50	50	100	2
18EC205	Circuit Theory	4	1	0	5	50	50	100	4
18PHL01	Physics lab	0	0	3	3	50	50	100	1
18ECL22	Programming with C ++ Lab	0	0	3	3	50	50	100	1
18ELL01	English Communication and Skills Lab	0	0	3	3	50	50	100	1
	TOTAL	19	1	9	29	400	400	800	18

CIE: Continuous Internal Evaluation   SEE: Semester End Examination

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SCHEME OF INSTRUCTION & EXAMINATION (Semester System)
For
Electronics and Communications Engineering
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Second Year B.Tech (SEMESTER – III)

Code No.	Subject	Scheme of Instruction (Periods per week)				Scheme of Examination (Maximum marks)			No. of Credits
		L	T	P	Total	CIE	SEE	Total Marks	
18MA003	Probability and Statistics	3	1	0	4	50	50	100	3
18EC302	Data Structures using Python	4	0	0	4	50	50	100	3
18EC303	Electronic Devices and Circuits	4	0	0	4	50	50	100	3
18EC304	Electromagnetic Field Theory	4	1	0	5	50	50	100	4
18EC305	Digital Electronics	4	1	0	5	50	50	100	4
18EL002	Technical English	3	0	0	3	50	50	100	2
18ECL31	Data Structures using Python Lab			3	3	50	50	100	1
18ECL32	Electronic Devices & Digital Electronics Lab			3	3	50	50	100	1
18ECL33	PSPICE Lab			3	3	50	50	100	1
	TOTAL	22	3	9	34	450	450	900	22

CIE: Continuous Internal Evaluation   SEE: Semester End Examination
L: Lecture,   T: Tutorial,   P: Practical

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SCHEME OF INSTRUCTION & EXAMINATION (Semester System)
For
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Effective from the Academic Year 2018-2019 (R18 Regulations)
Second Year B.Tech (SEMESTER – IV)

Code No.	Subject	Scheme of Instruction (Periods per week)				Scheme of Examination (Maximum marks)			No. of Credits
		L	T	P	Total	CIE	SEE	Total Marks	
18MA004	Complex Variables and Special Functions	3	1	0	4	50	50	100	3
18EC402	Electronic Circuit Analysis	4	0	0	4	50	50	100	3
18EC403	EM Waves and Transmission Lines	4	1	0	5	50	50	100	4
18EC404	Signals & Systems	4	1	0	5	50	50	100	4
18EC405	Digital Design Using HDL	4	1	0	5	50	50	100	4
18EC406	Professional Ethics and Human Values	4	0	0	4	50	50	100	3
18ECL41	Electronic Circuits Lab			3	3	50	50	100	1
18ECL02	HDL Lab			3	3	50	50	100	1
18ECL43	Signals and Systems lab			3	3	50	50	100	1
	TOTAL	23	4	9	36	450	450	900	24

CIE: Continuous Internal Evaluation   SEE: Semester End Examination
L: Lecture,   T: Tutorial,   P: Practical