

## **P.Vasudeva Reddy**

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### **Biography:**

P.Vasudeva Reddy received his M.Tech degree from NIT BHOPAL in 2015

His major areas of interest Low Power VLSI design.

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### **Publications**

1. "Comparative study of adaptive methodologies for extracting Abdominal fetal Electrocardiogram" in International journal of Research(IJR), Vol-04, Issue-08, July 2017