

DASARI GOPI CHAND

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Biography

Mr. Gopi chand completed his masters from VNIT Nagpur in VLSI Design in 2015.

His interested research areas are mixed signal design, analog design and antenna synthesis. He worked on several data converters and low power analog circuits and also during professional career and adapted them to antenna synthesis. He cleared GATE five times qualified UGC NET 3 times. He is member of IAENG professional body society.

Selected Publications

1. Sowmya, T., SK Muneer Nihal, and D. Gopi Chand. "IMPLEMENTATION OF 16-BIT PIPELINED ADC USING 180nm CMOS TECHNOLOGY." (2018).
2. Kumar, MN Naveen, and D. Gopi Chand. "STUDY OF PARAMETERS OF DIAMOND BASED DDR IMPATT DIODE." (2017).