IDRISH SHAIK

Assistant Professor

Electronics & Communication

Bapatla Engineering College, Bapatla.

Email: idrishshaik@gmail.com, Idrish.shaik@becbapatla.ac.in

Biography

Mr Sk. Idrish received his M.Tech from VIGNAN University in 2011. He worked as project trainee at DLRL, DRDO (2011) before joining Bapatla Engineering College. His research is in the broad area of Wireless Communications.

Awards &Honors

Selected Publications

1. Surendra Babu Bachina, **Idrish Shaik,** "PERFORMNACE OF ERROR CORRECTION CODES FOR 5G COMMUNICATIONS", International Conference on Micro-Electronics, Electromagnetics and Telecommunications (**ICMEET-2019 SPRINGER Conference**), 6th-7th December 2019.

2. K Prasanthi**, Sk Idrish**, "Development Of Software For Estimation Of Structural Dynamic Characteristics Of Mechanical Systems In Real Time", International Research Journal of Engineering and Technology (IRJET), Vol 4, pp 2197-2198, August 2017

3. Sharoze Ali, Habibulla Khan, **Idris Shaik**, Firoz Ali," Fast Phase Unwrapping Method Based On G-Puma And Spa Techniques: G-Puma-Spa", ARPN Journal of Engineering and Applied Sciences, ISSN 1819-6608, VOL. 11, NO. 3, Feb 2016.(Scopus Indexed).

4. K Krishna chaitanya, **Idrish Shaik,** Surendra Babu Bachina, "Design of Application Software for Receiver Processor Subsystem", International Conference on Advances in Signal Processing and Communications (NECICASPC-2016)", September 9-10, 2016.

5. Sharoze Ali, Habibulla Khan**, Idris Shaik**, Firoz Ali, "Very Fast Absolute Phase Estimation: G-PEARLS & I-PEARLS", International Journal of Engineering and Technology (IJET) ISSN: 0975-4024,Vol 7 No 5, pp 1754-1766 Oct-Nov 2015. (Scopous Indexed)