Profile of K.Kalpana

Name: K.Kalpana
Designation:Assitant Professor
Department of ECE,
Bapatla Engineering College,Bapatla.
✉:koppolukalpana@gmail.com

Experience: Teaching: 5 years; Industry: NIL

Research Interests: Wireless Communications

Educational Qualifications:

2012-2014	M. Tech. CESP-9.3(CGPA)	Bapatla Engineering College, Bapatla,AP
2006-2010	B. Tech. (ECE-78.22%)	Bapatla Engineering College, Bapatla,AP

Date of Joining in the Institution: 29/06/2015

Events Attended (FDPs / Workshops / Seminars / Orientation Programs):

- Attended a one week FDP on "Research opportunities in Image processing and Machine learning", conducted by CBIT Hyderabad

- Attended a One Day workshop on SCILAB conducted on **4th MAY 2019** at RVR&JC College of Engineering Conducted by the Teaching Learning Centre (ICT), Supported by FOSSEE & Spoken Tutorials, **IIT Bombay**

- Attended a Faculty Development Program on "DIGITAL SIGNAL PROCESSORS,EMBEDDED SYSTEM DESIGN & IOT " from 11-02-2019 to 15-02-2019 at Department of ECE, LBRCE, Mylavaram

- Attended a Faculty Development Program on "METHODOLOGIES FOR GENERATING REVENUE THROUGH RESEARCH WITH PATENTS & IPR " on 30-01-2019 at Bapatla Engineering College, Bapatla

- Attended a Faculty Development Program on "Pattern Recognition, Machine Learning & Computer Vision Applications using MATLAB with Hands-on Training " from 07-05-2018 to 11-05-2018 at Department of ECE, Vignan Foundation for Science & Technology, Guntur

- Participated in a Two Day National Workshop on "Signal Processing & Wireless Networks" on 21-12-2017 & 22-12-2017 organised by Department of ECE, Bapatla Engineering College, Bapatla

- Attended One day National convention on "Research Trends in wireless communication Technologies" at PVP SIT, VIJAYWADA on 29 July 2017.

- Attended Two day National Level workshop on" Design of small satellite antennas" at BWEC, Bapatla.

- Attended Two day National Level workshop on "Wireless Communication and Signal Processing" at BEC, Bapatla.

Publications:

- Published paper on "Speech modulation for Image Watermarking",JES JOURNAL ,Volume 11,Issue 05, May 2020.

- Published paper on "Multislot Microstrip Antenna Design in Ultra Wide Band Region using HFSS software", IRJET volume5 issue 3 March 2018

- Published paper on "Design and Development of Hardware and Firmware for a multifunctional SMD Frequency synthesizer", IJARCCE International Journal of Advanced Research in Computer and Communication Engineering,VOL.3 No.9, September,2014

Activities / Awards:

- Qualified in UGC NET for Assistant Professor in the subject of **ELECTRONIC SCIENCE on 08-07-2018**

- Conducted Two day National Level workshop on "Custom IC design" using Mentor EDA Tools at BEC, Bapatla on 17-18 April 2018

- Got Prathiba award by Government of Andhrapradesh in the year 2014 for excellence in M.tech examinations.

(K. KALPANA)