A. VIJAYA BHASKAR

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Biography:

I am from Rajahmundry, East Godavari District, Andhra Pradesh. I have completed my B.Tech from Sasi Institite of Technology and Engineering under JNTU Hyderabad and M.Tech from Sasi Institite of Technology and Engineering under JNTU Kakinada.

Currently I am pursuing my Ph.D from IIT Madras under the guidance of Dr. T.G.Venkatesh.

Awards & Honors:

1. Top of the class in B.Tech

2. Top of the class in M.Tech

3. Resource person at the Faculty development programme conducted by the ECE Department, NIT Andhra pradesh

4. Captain of Badminton team in Cauvery hostel, IIT Madras.

Selected Publications:

1. Vijaya Bhaskar Adusumilli, Venkatesh Vutukuri, Pavan Kumar Uppu, Swasthik Varsa and Ravi Kumar Thummala "Verification of SDRAM controller using SystemVerilog", IEEE CONECCT 2020

2. Vijaya Bhaskar Adusumilli, Akhil Repala, Jithendreswar Rao Koleti, Sathish Volladam and Arjun Goud Bolepalli, "Delay and Area analysis of hardware implementation of FFT using FPGA", IEEE CONECCT 2020

3. A. V. Bhaskar and T. G. Venkatesh, "A study of the effect of virtual channels on the performance of Network-on-Chip," 2015 IEEE Student Conference on Research and Development (SCOReD), Kuala Lumpur, 2015, pp. 255-260, doi: 10.1109/SCORED.2015.7449335.