

[Dr.N.KARTHIK](#)

Associate Professor

EEE Department

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## Biography

Dr.N.Karthik received his Ph.D from JNTUK, Kakinada in 2018. Currently working as Associate Professor in EEE Department Bapatla Engineering College Bapatla.

His research is in the broad area of power quality and renewable energy sources. His focus is on mathematical formulation and analytical solution of problems in power quality and FACTS devices. He has made significant research contributions on the understanding of the Power Quality improvement with the help of FACTS devices by using AI techniques. He is also engaged in department and college level administrative activities, also conducting continuing education program for participants from industry and academia.

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## Awards & Honors

1. Acted as Judge in paper presentation contest conducted by St.Anns College of Engineering, Chirala in 2018

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## Selected Publications

1. Mr.N.Karthik, Dr.Sk.Abdul Gafoor, Dr.M.Surya Kalavathi "Classification of Power quality problems by wavelet Fuzzy expert system" in Advanced Material Research Journal. Advanced Materials Research Vols. 463-464 (2012) pp 1573-1578 Trans Tech Publications, Switzerland. **(Scopus)**
2. Mr.N.Karthik, Dr.M.Surya Kalavathi "Power Quality Improvement and Harmonics Mitigation of Active Power Filter based on Cascaded Multilevel Converters" Journal of Electrical Engineering, Engineering, Vol.14, No.4, pp.165-171, November 2014. **(Scopus)** Politechnica Publishing House.
3. Mr.N.Karthik, Dr.M.Surya Kalavathi "ANFIS based UPQC using Cascaded H Bridge Converters for Power Quality Improvement" International Journal of Applied Engineering Research, ISSN 0973-4562 Vol. 10, No.1 (2015) pp. 87-94. **(Scopus)**
4. Mr.N.Karthik, Dr.M.Surya Kalavathi, "ANFIS based unified power-quality conditioner for power quality improvement in multi feeder system" International Journal of Engineering Intelligent Systems, Vol 3, No 4 Dec 2015 **(SCI Journal)**
5. Mr.N.Karthik, Dr.Sk.Abdul Gafoor, Dr.M.Surya Kalavathi "Shunt active Power Filter Based on cascaded multilevel inverter using ANFIS" at IEEE CON PCIE Nov 8-9th 2013, Hyderabad, published in **IET digital library**