

Profile of

**Mr. CH.SRIDHAR**



**Name:**

Ch.Sridhar  
Mobile: 9492871120

**Age:**

41 Years

**Permanent Address:**

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**Educational Qualifications:**

S.No	Degree	University	Year of passing	Division
1	(Ph.D) (Fault diagnosis schemes of 3-phase AC Machines)	ANU, Guntur	Pursuing	-
2	M.Tech Electrical Power Engg	JNTU, Hyderabad	2007	First(86)
3	B.Tech(EEE)	JNTU, Anantapur.	2002	First (70.8)
4	DEEE	SMVM Polytechnic,Tanuku	1997	First(75.2)
5	S.S.C	Z.P.P High School,Koppaka	1993	First(79.1)

**Administrative Experience** : EM-I Lab In-charge  
EEE Dept board of Studies in-charge.

**Teaching Experience** : 17years

S.No.	Instituion/Organizaton	Position held	Duration
1	Bapatla Engg. College Bapatla	Asst.Professor	07-07-2006 to present
2	Bapatla Polytechnic College, Bapatla	Lecturer	04-07-2004 to 30-04-2006
3	Sir CRR Polytechnic, Eluru	Lecturer	05-07-2003 to 30-04-2004

**Industrial Experience** 03years  
Working as a Quality controller in  
Zenith Hard Ware Industries, Anantapur.

Subjects taught at UG Level	Subjects taught at PG Level
1. Circuit Theory-I& II	1. Solid state power converters
2. Electrical Machines-I,II&III	
3. Industrial Drives	
4. Power Electronics	
5. Transmission & Distribution	
6. Utilization of Electric power	
7. Digital Signal Processing	

**Publications:**

- International Journals: 10
- International Conferences: 06
- National Conferences: 02

## Publications

### International Journals

<b>1.</b>	M.Ravi kumar, <b>Ch.Sridhar</b> “Design and Simulation of Phase Shifted Carrier Cascaded Multilevel Converter Based DSTATCOM for Compensation of Harmonics and Reactive Power” International Journal of Technology and Engineering Science [IJTES]TM Volume 2[10], pp: 2440-2443, October 2014
<b>2.</b>	M.Kattereni, <b>Ch.Sridhar</b> “Enhancement of Power Quality using Reduced Rating Dynamic Voltage Restorer with a Battery Energy Storage System”, International Journal of Technology and Engineering Science[IJTES] TM Volume 3[6], pp: 3533-3537, June 2015
<b>3.</b>	K.Mounika, <b>Ch.Sridhar</b> “Control schemes for Improving Power Quality with BESS and Ultra-Capacitor Supported Dynamic Voltage Restorer” International Journal of Advance Engineering and Research Development, Volume 4, Issue 4, April-2017
<b>4.</b>	BV.Rajanna,G.Jogarao, <b>Ch.Sridhar</b> , “Solar photovoltaic Generators with MPPT and battery storage in microgrids” 369-377, ISSN 2088-8694 International Journal of Power Electronics and Drive System (IJPEDS) ,communicated.
<b>5.</b>	A.Naresh, <b>Ch.sridhar</b> “Implementation of Multi level Inverter topology with Reduced Switches for Grid Interconnection of PV systems”Volume 04 Issue 09, p-ISSN:2348-6848,e-ISSN:2348-795X,August 2017,International Journal Research.
<b>6.</b>	Ch.Sridhar,Dr.N.Rama Devi, “Diagnosis of Stator winding inter-turn Fault in Wound-Field Synchronous Motor by using Wavelet Transform” Jour of Adv Research in Dynamical & Control Systems, Vol. 11, Issue-07, 2019 pp:571-578
<b>7.</b>	Ch.Sridhar,Dr.N.Rama Devi, “Detection and Identification of stator Incipient faults in Wound-field Synchronous Motor Using Wavelet transform” International Journal of Advanced Science and Technology Vol. 29, No. 4, (2020), pp. 904-913.
<b>8.</b>	Ch.Sridhar,Dr.N.Rama Devi, “Detection of Stator Incipient Faults in Wound-Field Synchronous Motor using Adaptive threshold Algorithm”, Jour of Adv Research in Dynamical & Control Systems, Vol. 12, 02-Issue, 2020 pp:2155-2165.
<b>9.</b>	Ch.Sridhar,Dr.N.Rama Devi, “Induction Motor Fault Diagnosis Using Wavelets and FFT”, International Journal of Test Engineering and Management, January - February 2020 ISSN: 0193 - 4120 Page No. 11340 - 11348
<b>10.</b>	Ch.Sridhar,Dr.N.Rama Devi, “Detection and Classification of Bearing and Single Phasing Faults of a Squirrel-Cage Induction Motor using Wavelet Transform”, International Journal of Test Engineering and Management,

### **Short term Courses/Workshops attended**

1. Attended in the two day workshop on “HVDC,FACTS AND INTELLIGENT CONTROL TECHNIQUES” conducted under TEQIP by the department of EEE, Bapatla Engineering College Bapatla during 8<sup>th</sup>-9<sup>th</sup> August 2005.
2. Attended a two work shop on “power system security” organized by the department of Electrical & Electronics Engineering of OU College of Engineering, Hyderabad from 14<sup>th</sup> September & 15<sup>th</sup> September 2009.
3. Participated in the National Level Faculty Development Program Under TEQIP-II Organized by Department of Electrical & Electronics Shri Vishnu College of Engineering For Women,Bhimavaram, during 3<sup>th</sup> and 4<sup>th</sup> August, 2002.
4. Participated in the “International Workshop on Trends in power System Protection and Control ” (TIPSAC-2013) during 19<sup>th</sup> & 20<sup>th</sup> July, 2013 organized by the Department of EEE, JNTU College of Engineering, Vizianagaram.
5. Attended a short term course on “Condition Monitoring of rotating machine elements” organized by the dept of mechanical Engg IIT,Indore during 21-22 December, 2015.