

Mr.G.RAJESH

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Biography

Mr.G.Rajesh had completed his under graduation(B.Tech) in the year 2005. He received his PG degree(M.Tech) in the area of Power Systems Engineering in the year 2009. He Pursuing his Ph.d in Annamalai University-Chidambaram. His research interests are in the area of Power Quality,Power system protection.He has a total teaching experience of 14 years . Currently working as a Assistant Professor in EEE Department Bapatla Engineering College-Bapatla.

His focus is on mathematical formulation and analytical solution of problems in power Quality and FACTS Controllers.He is also engaged in departmental and college level Administrative activities.

Selected Publications

1. Harmonics Reduction and Power Quality Improvement by Using Multilevel DPFC by Ms.M.Sujitha, Mr.B.Vijaya Krishna, Mr.G.Rajesh ,2016 , IJEDR,Volume4,issue3
2. Reduction of Harmonics &Voltage Flicker Compensation by STATCOM by Ms.D.Girija, Mr.G.Rajesh , June 2015,IJTES,Volume3(6) pp:3517-3526.