# K.Rajendra

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# Biography

Mr. K.Rajendra currently Pursuing his Ph.D from Annamalai University – Chidambaram, Chennai. He Received his M. Tech from NIT, Kurukshetra in the year 2009 in Power systems Engineering specialisation.

His area of research work includes Optimization techniques in various power system applications, and also Controller parameter optimization and design in multi-area power system.

# **Selected Publications**

#### **International Journal: Three**

- 1. Published paper on "SIMULATION OF DYNAMIC VOLTAGE RESTORER IN POWER SYSTEM" in International Journal of Applied Engineering Research
- 2. Published paper on "SIMULATION OF AGC IN DEREGULATED ENVIRONMENT" in International Journal of Applied Engineering Research
- 3. **Published** paper on "IMPROVEMENT OF VARIOUS DISTURBANCES IN POWER SYSTEM USING DYNAMIC VOLTAGE RESTORER" in International e-Journal of Mathematics and Engineering 2 (2010) 15-31

## **International Conferences: One**

 Mr.Ch.Santhan,Mr.B.Shanker,Mr.K.Rajendra,P.Sampath" Power Systems Voltage Stability Enhancement in a Wind Generators Connected Electrical Network in Increased Load Demand using FACTS Device " IEEE-ICRDPET-2013 at E.G.S Pillay Engineering College,Nagapattinam.



## National conference: One

 Mr.K.Rajendra, P.Venkatesh "A Nero fuzzy control approach for automatic generation control Of Interconnected power systems in Deregulated Environment" at Technological Developments in Power Engineering 2012, Vignan University, Vadlamudi, Guntur (dt).

# Workshops Attended: Four

- A Two day National Workshop on "New Trends in NANO TECHNOLOGY" HCTM Kaithal, Haryana from 20<sup>th</sup> to 21<sup>st</sup> Sep, 2009
- A Two day National Workshop on "MatLab Applications in Power Engineering" at Adishankara Engineering College, Nellore from 9th to 10th Dec, 2010
- **3.** A Short Term Course on **"FPGA BASED SYATEM DESIGN" at NIT Tiruchirapalli,** from 13<sup>th</sup> to 14<sup>th</sup> May 2012.
- 4. A Short Term Course on "Modeling of Electrical Systems Using Marlab/Simulink" at NIT Tiruchirapalli, from 21<sup>st</sup> to 22<sup>nd</sup> July 2012.