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III/IV B.Tech (Regular) DEGREE EXAMINATION

Feb, 2021

Fifth Semester

Time: Three Hours

Information Technology

Automata & Compiler Design

Maximum: 50 Marks

Answer all questions from Part-A.

(1X10= 10 Marks)

Answer ANY FOUR questions from Part-B.

(4X10=40 Marks)

Part-A

1. Answer all questions (1X10=10 Marks)
- Define deterministic finite automata. CO1
 - Define ϵ -closure of a state. CO1
 - What is the relation between $\Sigma^* = \Sigma^+$ CO1
 - What is ambiguous grammar? CO2
 - How many ways can PDA accepts the string? CO2
 - Define compiler. CO3
 - What are the difficulties with Top-Down parsing? CO3
 - List the possible actions can make in shift-reduce parsing. CO3
 - What are the different types of intermediate representations? CO4
 - Define basic block? CO4

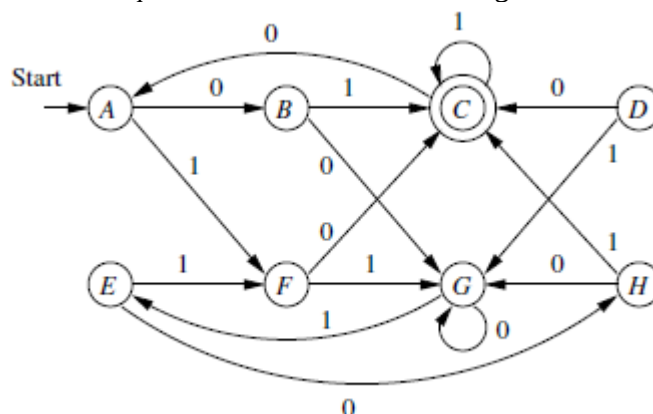
Part-B

UNIT-I

2. a) Design DFA to accept the language $L = \{w/w \text{ has both an even number of 0's and even number of 1's}\}$. 5M
CO1
- b) Construct a DFA equivalent to the NFA given by $M = (\{p,q,r,s\}, \{0,1\}, \delta, p, \{s\})$, where δ is defined in the following table 5M
CO1

δ	0	1
$\rightarrow p$	$\{p,q\}$	$\{p\}$
q	$\{r\}$	$\{r\}$
r	$\{s\}$	Φ
*s	$\{s\}$	$\{s\}$

3. a) Construct the minimum state equivalent DFA for the following CO1 5M



- b) Using pumping lemma prove $L = \{0^m 1^n \mid m < n, n \geq 1\}$ is not regular. CO1 5M

UNIT-II

4. a) Consider the following grammar CO2 5M
 $S \rightarrow A1B$
 $A \rightarrow 0A \mid \epsilon$
 $B \rightarrow 0B \mid 1B \mid \epsilon$
 Give leftmost and rightmost derivations of the following string **00101**
- b) Convert the grammar CO2 5M
 $S \rightarrow 0S1 \mid A$
 $A \rightarrow 1A0 \mid S \mid \epsilon$ to a PDA that accepts the same language by empty stack.

5. a) Construct a PDA that accepts the language $L = \{ WCW^R / W \in \{a, b\}^* \}$. CO2 5M
 b) Convert the following grammar into CNF from $G = (\{S, A, B\}, \{a, b, c\}, p, S)$ productions are 5M
 $S \rightarrow ABa$ CO2
 $A \rightarrow aab$
 $B \rightarrow Ac$

UNIT-III

6. a) Explain the output of each phase of a compiler for the statement CO3 5M
 “Position = Initial + rate * 60.0”
 b) Explain the role of lexical analyzer. CO3 5M
7. a) Test whether the grammar is LL(1) or not, and construct a predictive parsing table for $E \rightarrow E+T/$ 5M
 $T, T \rightarrow T*F/ F, F \rightarrow (E)/ id$ CO3
 b) Explain the stack implementation of Shift – reduce parser with an example. CO3 5M

UNIT-IV

8. a) What is a three address code? Generate quadruples, triples and indirect triples for the 5M
 expression $w := a* - (b + c)$ CO4
 b) What are the different storage allocation strategies available? Explain each in brief? CO4 5M
9. a) Define basic block and flow graph. Write an algorithm to construct basic block. CO4 5M
 b) Write an algorithm for simple code generation and construct code sequence for the following 5M
 expression: $w := (a-b) + (a-c) + (a-c)$ using code-generator algorithm. CO4



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Scheme of Evaluation & Answers

Part-A

1 Answer all questions

(1X10=10 Marks)

a) Define deterministic finite automata.

A DFA is represented formally by a 5-tuple, $(Q, \Sigma, \delta, q_0, F)$, consisting of

- a finite set of states Q
- a finite set of input symbols Σ
- a transition function $\delta: Q \times \Sigma \rightarrow Q$
- an initial (or start) state $q_0 \in Q$
- a set of states F distinguished as accepting (or final) states $F \subseteq Q$.

b) Define ϵ -closure of a state.

Epsilon (ϵ) – closure: Epsilon closure for a given state X is a set of states which can be reached from the states X with only (null) or ϵ moves including the state X itself.

c) What is the relation between $\Sigma^* = \Sigma^+$

Σ^* (Kleene star) is a unary operator on a set Σ of symbols or strings that gives an infinite collection of all possible strings of all possible lengths including λ (empty string). Σ^+ is same as Σ^* excluding empty string λ .

d) What is ambiguous grammar?

Ambiguous grammar: A CFG for which there are more than one left-most | right-most derivation trees or more than one derivation trees for a given string.

e) How many ways can PDA accept the string?

Two ways. i) Acceptance by final state. ii) Acceptance by empty stack.

f) Define compiler.

A compiler is a computer program that translates computer code written in one programming language (the source language) into another language (the target language).

g) What are the difficulties with Top-Down parsing?

- Backtracking.
- Left recursion.
- Left factoring.
- Ambiguity.

h) List the possible actions that can be made in shift-reduce parsing.

- 1) Shift
- 2) Reduce
- 3) Accept
- 4) Error

i) **What are the different types of intermediate representations?**

Syntax tree

Postfix notation (or) Reverse polish notation

Intermediate code

j) **Define basic block?**

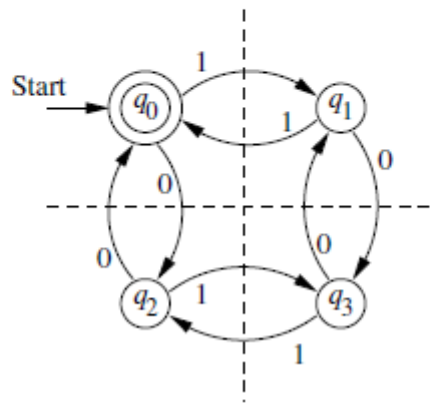
Basic Block is a straight line code sequence which has no branches in and out branches except to the entry and at the end respectively. Basic Block is a set of statements which always executes one after other, in a sequence.

Part-B

UNIT-I

2 a) **Design DFA to accept the language L where $L = \{w/w \text{ has both an even number of 0's and even number of 1's}\}$. 5M**

State q_0 is both the start state and the lone accepting state. It is the start state, because before reading any inputs, the numbers of 0's and 1's seen so far are both zero, and zero is even. It is the only accepting state, because it describes exactly the condition for a sequence of 0's and 1's to be in language L .



The DFA for language L is

$$A = (\{q_0, q_1, q_2, q_3\}, \{0, 1\}, \delta, q_0, \{q_0\})$$

Where the transition function δ is described by the transition diagram

	0	1
$* \rightarrow q_0$	q_2	q_1
q_1	q_3	q_0
q_2	q_0	q_3
q_3	q_1	q_2

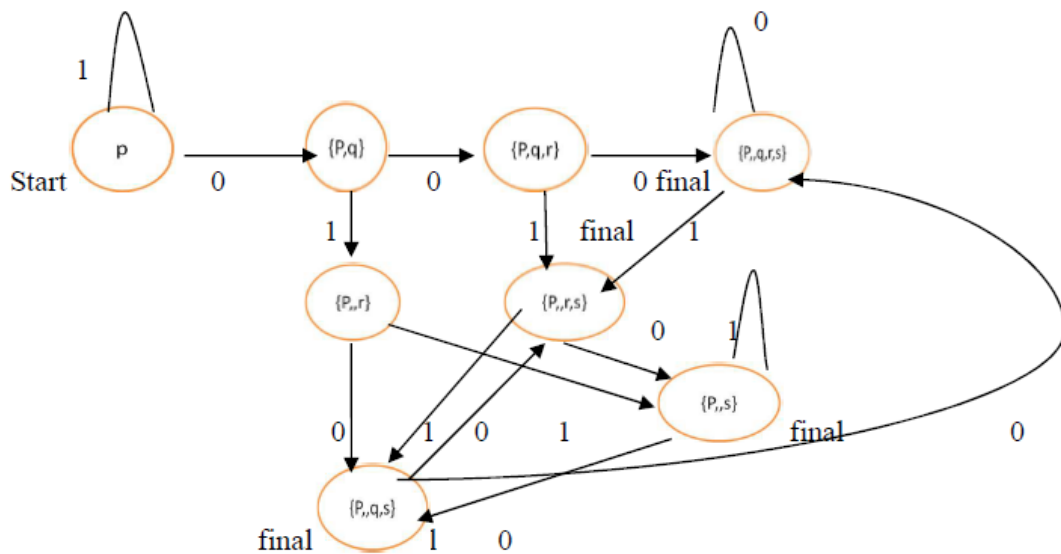
2 b) **Construct a DFA equivalent to the NFA given by $M = (\{p, q, r, s\}, \{0, 1\}, \delta, p, \{s\})$, where δ is defined in the following table 5M**

δ	0	1
$\rightarrow p$	$\{p, q\}$	$\{p\}$
q	$\{r\}$	$\{r\}$
r	$\{s\}$	Φ
$*s$	$\{s\}$	$\{s\}$

The following table represents NFA to DFA conversion :

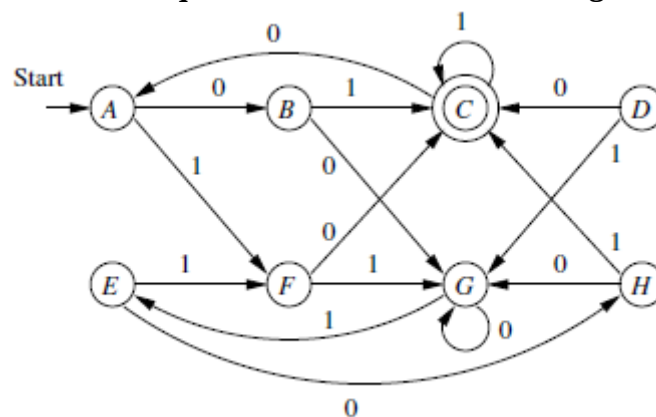
δ	0	1
$\rightarrow p$	{p,q}	{p}
{p,q}	{p,q,r}	{p,r}
{p,q,r}	{p,q,r,s}	{p,r,s}
*{p,q,r,s}	{p,q,r,s}	{p,r,s}
{p,r}	{p,q,s}	{p,s}
*{p,r,s}	{p,q,s}	{p,s}
*{p,q,s}	{p,q,r,s}	{p,r,s}
*{p,s}	{p,q,s}	{p,s}

Transition diagram



3 a) Construct the minimum state equivalent DFA for the following

5M



For the basis, since C is the only accepting state, we put x in each pair that involves C.

<i>B</i>							
<i>C</i>	<i>x</i>	<i>x</i>					
<i>D</i>			<i>x</i>				
<i>E</i>			<i>x</i>				
<i>F</i>			<i>x</i>				
<i>G</i>			<i>x</i>				
<i>H</i>			<i>x</i>				
	<i>A</i>	<i>B</i>	<i>C</i>	<i>D</i>	<i>E</i>	<i>F</i>	<i>G</i>

Now, we know some distinguishable pairs, we can discover others.

Consider pair (A,B):
in (A,B) $\left. \begin{array}{l} \delta(A,0) \rightarrow B \\ \delta(B,0) \rightarrow G \end{array} \right\} \text{X}$ $\left. \begin{array}{l} \delta(A,1) \rightarrow F \\ \delta(B,1) \rightarrow C \end{array} \right\} \rightarrow \text{Put X}$

Next Pair (A,D): $\left. \begin{array}{l} \delta(A,0) \rightarrow B \\ \delta(D,0) \rightarrow C \end{array} \right\} \rightarrow \text{Put X in (A,D) Pair}$

Next pair (A,E): $\left. \begin{array}{l} \delta(A,0) \rightarrow B \\ \delta(E,0) \rightarrow H \end{array} \right\} \text{X}$ $\left. \begin{array}{l} \delta(A,1) \rightarrow F \\ \delta(E,1) \rightarrow F \end{array} \right\} \text{X}$

Next pair (A,F): $\left. \begin{array}{l} \delta(A,0) \rightarrow B \\ \delta(F,0) \rightarrow C \end{array} \right\} \rightarrow \text{Put X in (A,F) Pair}$

Next Pair (A,G): $\left. \begin{array}{l} \delta(A,0) \rightarrow B \\ \delta(G,0) \rightarrow G \end{array} \right\} \text{X}$ $\left. \begin{array}{l} \delta(A,1) \rightarrow F \\ \delta(G,1) \rightarrow E \end{array} \right\} \text{X}$

Next Pair (A,H): $\left. \begin{array}{l} \delta(A,0) \rightarrow B \\ \delta(H,0) \rightarrow G \end{array} \right\} \text{X}$ $\left. \begin{array}{l} \delta(A,1) \rightarrow F \\ \delta(H,1) \rightarrow C \end{array} \right\} \rightarrow \text{Put X in (A,H)}$

Continue like this for remaining pairs also....

i.e (B,D), (B,E), (B,F), (B,G), (B,H), (D,E), (D,F), (D,G), (D,H), (E,F), (E,G), (E,H), (F,G), (F,H), and (G,H).

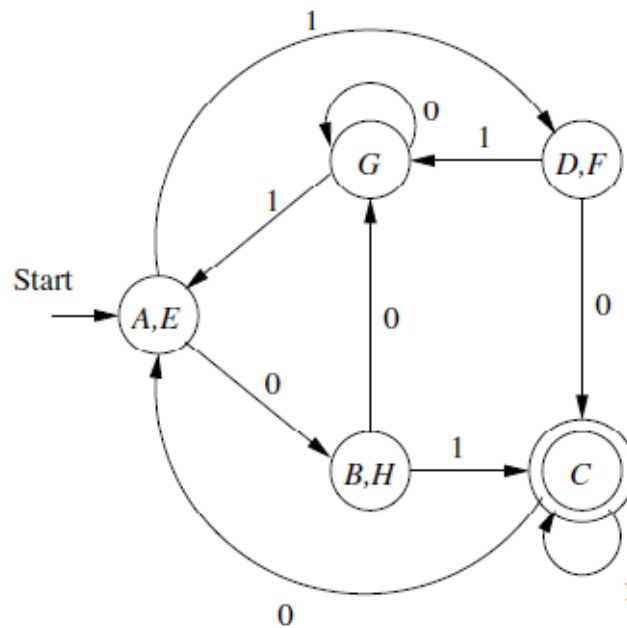
Observe the above table (A,E), (A,G), (B,H), (D,F), and (E,G) pairs are empty.

consider pair (A,G) $\left. \begin{array}{l} \delta(A,0) \rightarrow B \\ \delta(G,0) \rightarrow G \end{array} \right\} \rightarrow (B,G) \text{ distinguishable pair or filled with 'X'}$
So, pair (A,G) also distinguishable pair.
Consider pair (E,G) $\left. \begin{array}{l} \delta(E,0) \rightarrow H \\ \delta(G,0) \rightarrow G \end{array} \right\} \rightarrow (H,G) \text{ distinguishable pair or filled with 'X'}$
So, pair (E,G) also distinguishable pair.

<i>B</i>	<i>x</i>						
<i>C</i>	<i>x</i>	<i>x</i>					
<i>D</i>	<i>x</i>	<i>x</i>	<i>x</i>				
<i>E</i>		<i>x</i>	<i>x</i>	<i>x</i>			
<i>F</i>	<i>x</i>	<i>x</i>	<i>x</i>		<i>x</i>		
<i>G</i>	<i>x</i>	<i>x</i>	<i>x</i>	<i>x</i>	<i>x</i>	<i>x</i>	
<i>H</i>	<i>x</i>		<i>x</i>	<i>x</i>	<i>x</i>	<i>x</i>	<i>x</i>
	<i>A</i>	<i>B</i>	<i>C</i>	<i>D</i>	<i>E</i>	<i>F</i>	<i>G</i>

Hence, (A,G), (B,H), and (D,F) are equivalent states.

So, Minimum State DFA is:



3 b) Using pumping lemma prove $L = \{ 0^m 1^n \mid m < n, n \geq 1 \}$ is not regular. 5M

Let L be a regular language. Then there exists a constant 'c' such that for every string w in L such that $|w| \geq c$.

We can break the string w into three parts, $w = xyz$, such that

- $|y| > 0$
- $|xy| \leq c$
- For all $k \geq 0$, the string xy^kz is also in L .

Consider the string from L

$$w = 0000111 = xyz$$

$$x = 000; \quad y = 01; \quad z = 11$$

$$w = xy^i z = 000 (01)^i 11$$

If $i = 2$ then 000010100 not belongs to L

Hence, the given language is not regular.

4 a) Consider the following grammar

5M

$S \rightarrow A1B$

$A \rightarrow 0A \mid \epsilon$

$B \rightarrow 0B \mid 1B \mid \epsilon$

Give leftmost and rightmost derivations of the following string **00101**

LMD: for string 00101

$S \rightarrow A1B$
 $\rightarrow 0A1B$
 $\rightarrow 00A1B$
 $\rightarrow 00\epsilon 1B$
 $\rightarrow 0010B$
 $\rightarrow 00101B$
 $\rightarrow 00101\epsilon$
 $\rightarrow 00101$

RMD: for string 00101

$S \rightarrow A1B$
 $\rightarrow A10B$
 $\rightarrow A101B$
 $\rightarrow A101\epsilon$
 $\rightarrow 0A101$
 $\rightarrow 00A101$
 $\rightarrow 00\epsilon 101$
 $\rightarrow 00101$

4 b) Convert the grammar

5M

$S \rightarrow 0S1 \mid A$

$A \rightarrow 1A0 \mid S \mid \epsilon$ to a PDA that accepts the same language by empty stack.

1. For each variable A ,

$$\delta(q, \epsilon, A) = \{(q, \beta) \mid A \rightarrow \beta \text{ is a production of } G\}$$

2. For each terminal a , $\delta(q, a, a) = \{(q, \epsilon)\}$.

Variables in the given grammar are: S and A

$$\delta(q, \epsilon, S) = \{(q, 0S1)\}$$

$$\delta(q, \epsilon, S) = \{(q, A)\}$$

$$\delta(q, \epsilon, A) = \{(q, 1A0)\}$$

$$\delta(q, \epsilon, A) = \{(q, S)\}$$

$$\delta(q, \epsilon, A) = \{(q, \epsilon)\}$$

Terminals in the given grammar are: 0 and 1

$$\delta(q, 0, 0) = \{(q, \epsilon)\}$$

$$\delta(q, 1, 1) = \{(q, \epsilon)\}$$

5 a) Construct a PDA that accepts the language $L = \{WCW^R \mid W \in \{a, b\}^*\}$.

5M

Some string will come followed by one 'c', followed by reverse of the string before 'c'.

So we get to know that 'c' will work as an alarm to starting popping STACK.

So we will pop every 'a' with 'a' and every 'b' with 'b'.

For every two a's and b's push them into STACK

When 'c' comes do nothing.

Starting popping STACK: 'a' for 'a' and 'b' for 'b'.

$$\delta(q_0, a, Z) = (q_0, aZ)$$

$$\delta(q_0, a, a) = (q_0, aa)$$

$$\delta(q_0, b, Z) = (q_0, bZ)$$

$$\delta(q_0, b, b) = (q_0, bb)$$

$$\delta(q_0, a, b) = (q_0, ab)$$

$$\delta(q_0, b, a) = (q_0, ba)$$

// this is decision step

$$\delta(q_0, c, a) = (q_1, a)$$

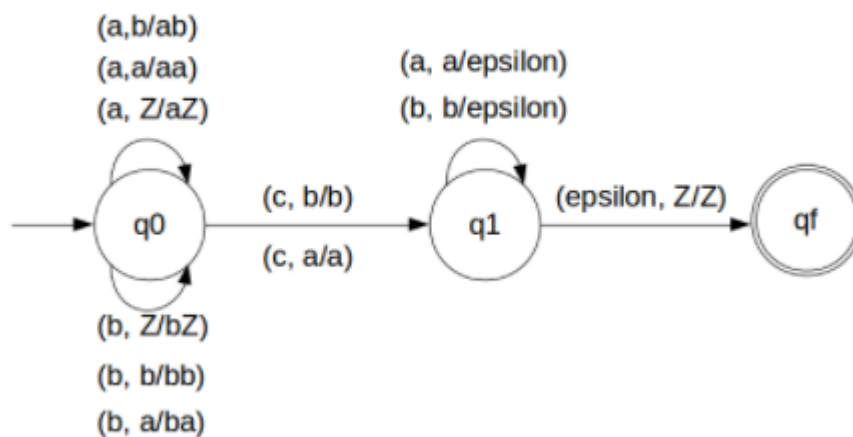
$$\delta(q_0, c, b) = (q_1, b)$$

$$\delta(q_1, b, b) = (q_1, \epsilon)$$

$$\delta(q_1, a, a) = (q_1, \epsilon)$$

$$\delta(q_1, \epsilon, Z) = (q_f, Z)$$

We have designed the PDA for the problem:



5 b) Convert the following grammar into CNF from $G = (\{S, A, B\}, \{a, b, c\}, p, S)$ productions 5M
are

$S \rightarrow ABa$

$A \rightarrow aab$

$B \rightarrow Ac$

A CFG is in Chomsky Normal Form if the Productions are in the following forms –

Non terminal $\rightarrow$ non terminal . non terminal (or)

Non terminal $\rightarrow$ terminal

Example: Conversion to CNF

i) Removal of null productions

There are no null productions

ii) Remove unit productions

There are no unit productions

iii) Conversion of each production

Replace each production $A \rightarrow B_1 \dots B_n$ where $n > 2$ with $A \rightarrow B_1 C$ where $C \rightarrow B_2 \dots B_n$.

Repeat this step for all productions having two or more symbols in the right side.

$S \rightarrow ABA$ can be written as $S \rightarrow AC \quad C \rightarrow BD \quad D \rightarrow a$

$A \rightarrow aab$ can be written as $A \rightarrow DE \quad E \rightarrow DF \quad F \rightarrow b$

$B \rightarrow Ac$ can be written as $B \rightarrow AG \quad G \rightarrow c$

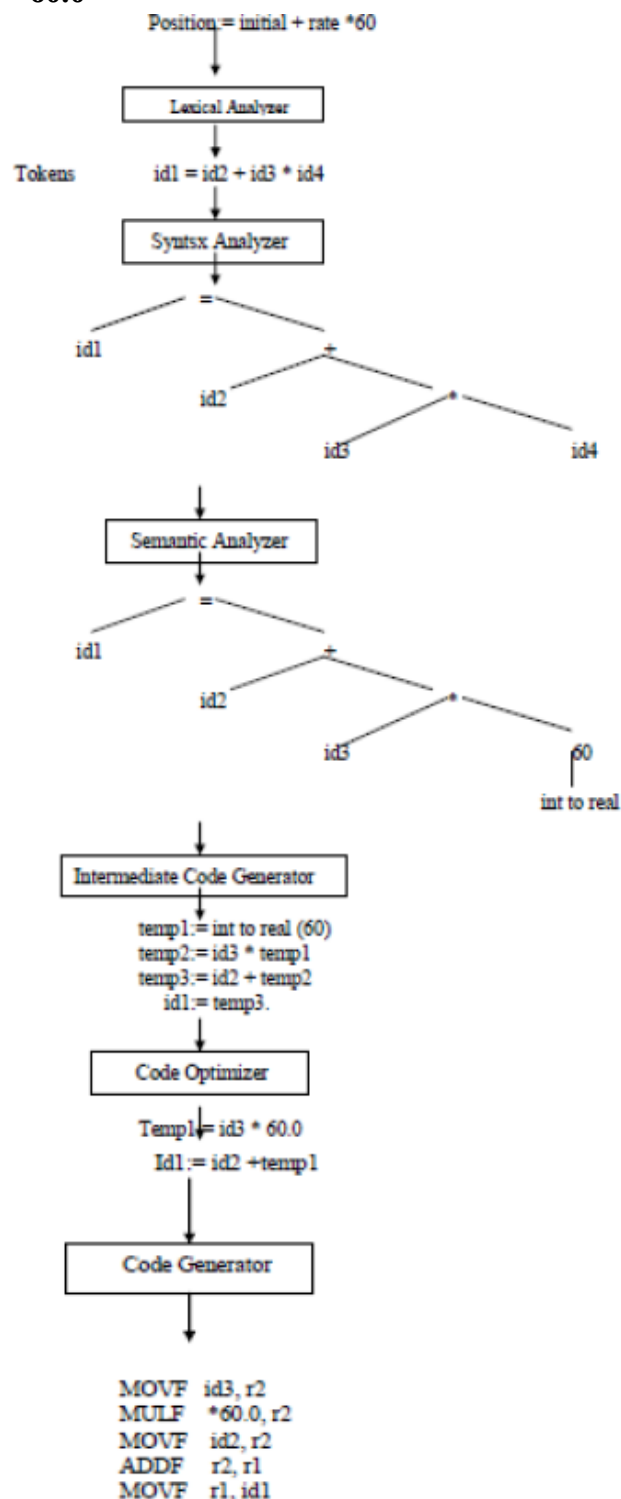
The final CFG in Chomsky normal form is

$S \rightarrow AC \quad C \rightarrow BD \quad D \rightarrow a \quad A \rightarrow DE \quad E \rightarrow DF \quad F \rightarrow b \quad B \rightarrow AG \quad G \rightarrow c$

UNIT-III

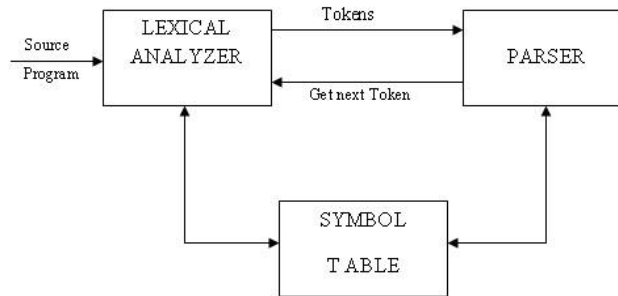
- 6 a) Explain the output of each phase of a compiler for the statement
"Position = Initial + rate * 60.0"

5M



6 b) Explain the role of lexical analyzer.**5M**

The LA is the first phase of a compiler. Its main task is to read the input character and produce as output a sequence of tokens that the parser uses for syntax analysis.



Upon receiving a 'get next token' command from the parser, the lexical analyzer reads the input character until it can identify the next token. The LA return to the parser representation for the token it has found. The representation will be an integer code, if the token is a simple construct such as parenthesis, comma or colon.

LA may also perform certain secondary tasks as the user interface. One such task is stripping out from the source program the commands and white spaces in the form of blank, tab and new line characters. Another is correlating error message from the compiler with the source program.

7 a) Test whether the grammar is LL(1) or not, and construct a predictive parsing table for **5M**
 $E \rightarrow E+T / T, T \rightarrow T * F / F, F \rightarrow (E) / id$

First eliminate the left recursion for E as

$$E \rightarrow TE' \\ E' \rightarrow +TE' \mid \varepsilon$$

Then eliminate for T as

$$T \rightarrow FT' \\ T' \rightarrow *FT' \mid \varepsilon$$

Thus the obtained grammar after eliminating left recursion is

$$E \rightarrow TE' \\ E' \rightarrow +TE' \mid \varepsilon \\ T \rightarrow FT' \\ T' \rightarrow *FT' \mid \varepsilon \\ F \rightarrow (E) \mid id$$

First() :

$$\begin{aligned} \text{FIRST}(E) &= \{ (, id \} \\ \text{FIRST}(E') &= \{ +, \varepsilon \} \\ \text{FIRST}(T) &= \{ (, id \} \\ \text{FIRST}(T') &= \{ *, \varepsilon \} \\ \text{FIRST}(F) &= \{ (, id \} \end{aligned}$$

Follow():

$$\begin{aligned} \text{FOLLOW}(E) &= \{ \$,) \} \\ \text{FOLLOW}(E') &= \{ \$,) \} \\ \text{FOLLOW}(T) &= \{ +, \$,) \} \\ \text{FOLLOW}(T') &= \{ +, \$,) \} \\ \text{FOLLOW}(F) &= \{ +, *, \$,) \} \end{aligned}$$

Predictive parsing table :

NON-TERMINAL	id	+	*	(	)	\$
E	$E \rightarrow TE'$			$E \rightarrow TE'$		
E'		$E' \rightarrow +TE'$			$E' \rightarrow \epsilon$	$E' \rightarrow \epsilon$
T	$T \rightarrow FT'$			$T \rightarrow FT'$		
T'		$T' \rightarrow \epsilon$	$T' \rightarrow *FT'$		$T' \rightarrow \epsilon$	$T' \rightarrow \epsilon$
F	$F \rightarrow id$			$F \rightarrow (E)$		

The parsing table has no multiply defined entries.

Hence, the given grammar is LL(1) grammar.

7 b) **Explain the stack implementation of Shift – reduce parser with an example.**

5M

Actions in shift-reduce parser:

- shift – The next input symbol is shifted onto the top of the stack
- reduce – The parser replaces the handle within a stack with a non-terminal.
- accept – The parser announces successful completion of parsing.
- error – The parser discovers that a syntax error has occurred and calls an error recovery routine.

Stack implementation of shift-reduce parsing :

Stack	Input	Action
\$	$id_1 id_2 * id_3 \$$	shift
\$ id_1	$+ id_2 * id_3 \$$	reduce by $E \rightarrow id$
\$ E	$+ id_2 * id_3 \$$	shift
\$ E +	$id_2 * id_3 \$$	shift
\$ E + id_2	$* id_3 \$$	reduce by $E \rightarrow id$
\$ E + E	$* id_3 \$$	shift
\$ E + E *	$id_3 \$$	shift
\$ E + E * id_3	$\$$	reduce by $E \rightarrow id$
\$ E + E * E	$\$$	reduce by $E \rightarrow E * E$
\$ E + E	$\$$	reduce by $E \rightarrow E + E$
\$ E	$\$$	accept

UNIT-IV

8 a) **What is a three address code? Generate quadruples, triples and indirect triples for the expression $w := a * - (b + c)$** 5M

Three address code is a type of intermediate code which is easy to generate and can be easily converted to machine code. It makes use of at most three addresses and one operator to represent an expression and the value computed at each instruction is stored in temporary variable generated by compiler.

Three address code for the given expression: $T1 := b + c$

$T2 := \text{uminus } T1$

$T3 := a * T2$

$w := T3$

Quaruples:

OP	arg1	arg2	Result
+	b	c	T1
uminus	T1	-	T2
*	a	T2	T3
assign	T3	-	w

Triples:

	OP	arg1	arg2
(0)	+	b	c
(1)	uminus	T1	-
(2)	*	a	(1)
(3)	assign	w	(2)

8 b) What are the different storage allocation strategies available? Explain each in brief?

5M

The different ways to allocate memory are:

1. Static storage allocation
2. Stack storage allocation
3. Heap storage allocation

Static storage allocation

- In static allocation, names are bound to storage locations.
- If memory is created at compile time then the memory will be created in static area and only once.
- Static allocation supports the dynamic data structure that means memory is created only at compile time and deallocated after program completion.
- The drawback with static storage allocation is that the size and position of data objects should be known at compile time.
- Another drawback is restriction of the recursion procedure.

Stack Storage Allocation

- In static storage allocation, storage is organized as a stack.
- An activation record is pushed into the stack when activation begins and it is popped when the activation ends.
- Activation record contains the locals so that they are bound to fresh storage in each activation record. The value of locals is deleted when the activation ends.
- It works on the basis of last-in-first-out (LIFO) and this allocation supports the recursion process.

Heap Storage Allocation

- Heap allocation is the most flexible allocation scheme.
- Allocation and deallocation of memory can be done at any time and at any place depending upon the user's requirement.
- Heap allocation is used to allocate memory to the variables dynamically and when the variables are no more used then claim it back.
- Heap storage allocation supports the recursion process.

9 a) Define basic block and flow graph. Write an algorithm to construct basic block. 5M

Basic block is a set of statements that always executes in a sequence one after the other.

Flow Graph is a directed **graph** with **flow** control information added to the **basic blocks**.

Basic Block Construction:

Algorithm: Partition into basic blocks

Input: A sequence of three-address statements

Output: A list of basic blocks with each three-address statement in exactly one block

Method:

1. We first determine the set of *leaders*, the first statements of basic blocks. The rules we use are of the following:
 - a. The first statement is a leader.
 - b. Any statement that is the target of a conditional or unconditional goto is a leader.
 - c. Any statement that immediately follows a goto or conditional goto statement is a leader.
2. For each leader, its basic block consists of the leader and all statements up to but not including the next leader or the end of the program.

9 b) Write an algorithm for simple code generation and construct code sequence for the following expression: $w := (a-b) + (a-c) + (a-c)$ using code-generator algorithm. 5M

For an instruction $x = y \text{ OP } z$, the code generator may perform the following actions. Let us assume that L is the location (preferably register) where the output of $y \text{ OP } z$ is to be saved:

- Call function `getReg`, to decide the location of L .
- Determine the present location (register or memory) of y by consulting the Address Descriptor of y . If y is not presently in register L , then generate the following instruction to copy the value of y to L :

`MOV  $y'$ ,  $L$` where y' represents the copied value of y .

- Determine the present location of z using the same method used in step 2 for y and generate the following instruction:

`OP  $z'$ ,  $L$` where z' represents the copied value of z .

- Now L contains the value of $y \text{ OP } z$, that is intended to be assigned to x . So, if L is a register, update its descriptor to indicate that it contains the value of x . Update the descriptor of x to indicate that it is stored at location L .
- If y and z has no further use, they can be given back to the system.

The expression $W = (A - B) + (A - C) + (A - C)$ might be translated in to the following three address code:

$t := a - b$

$u := a - c$

$v := t + u$

$d := v + u$ with d live at the end.

Code sequence for the example is:

Statements	Code Generated	Register descriptor	Address descriptor
		Register empty	
$t := a - b$	MOV a, R ₀ SUB b, R ₀	R ₀ contains t	t in R ₀
$u := a - c$	MOV a, R ₁ SUB c, R ₁	R ₀ contains t R ₁ contains u	t in R ₀ u in R ₁
$v := t + u$	ADD R ₁ , R ₀	R ₀ contains v R ₁ contains u	u in R ₁ v in R ₀
$d := v + u$	ADD R ₁ , R ₀ MOV R ₀ , d	R ₀ contains d	d in R ₀ d in R ₀ and memory

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