14CS602

Η	Hall Ticket Number:						
		III/IV B.Tech (Regular / Supplementary-Repeat Exam) DEGREE EXAMINATION					
January, 2021 Computer Science & Engineering							
Si	Sixth Semester Compiler Design						
Ti	Time: Three Hours Maximum: 60 Marks						
Answer All Questions from Part- A.(1X12 = 12 Marks)Answer ANY FOUR Questions from Part - B.(4X12 = 48 Marks)							
1	Ang	Part -A (1V12-12 M	ortra)				
	a) b) c) d) e) f) g) h) i) j) k) l)	Define phase. Define compiler. What is the role of the lexical analyser? Define DAG? Define synthesized attribute? Define annotated parse tree? List out parameter passing techniques. What is an activation record. Define symbol table. What is the instruction cost of the instruction ADD *R ₀ , 4(R ₁). What is basic block? What is backbatching?					
1	a)	Part - B	6M				
1.	a) b)	Explain input buffering.	6M				
2.	a)	Find out FIRST AND FOLLOW sets for each nonterminal of the following grammar? $P \rightarrow AQRbe \mid mn \mid Dei$ $A \rightarrow ab \mid e$ $Q \rightarrow q_1q_2 \mid e$ $R \rightarrow r_1r_2 \mid e$ $D \rightarrow d$ $F \rightarrow e$	6M				
	b)	Construct predictive parsing table for the following grammar and check whether the grammar is LL(1). $S \rightarrow L=R \mid R$ $L \rightarrow *R \mid ID$ $R \rightarrow L$	6M				
3.		Construct LR(0) item sets for the following grammar and then construct SLR parsing table. $E \rightarrow E+T \mid T$ $T \rightarrow T^*F \mid F$ $F \rightarrow (E) \mid ID$	12M				
4.		Construct CLR parsing table for the following grammar? $S \rightarrow aTRe$ $T \rightarrow Tbc \mid b$ $R \rightarrow d$	12M				
5.	a) b)	Explain different data structures to implement symbol table. Explain fields of the activation record.	8M 4M				

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6.	a)	Explain different storage allocation strategies in detail.	12M
7.	a) b)	Explain different intermediate languages. Give SDT scheme for Boolean expressions using back patching. Generate three address statements for $(a < b) \&\& (c < d) (e < f)$.	6M 6M
8.	a) b)	Explain code generation algorithm. Generate code for the following by using above algorithm. t=a-b u=a-c v=t+u d=u+v	6M 6M