


FACULTY PROFILE

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|---|---|----------------------------|---------------------|---|
| Name of the Faculty: | | YARLAGADDA SRUTHI | |  |
| Designation: | | Assistant Professor | | |
| Department: | | ECE | | |
| Date of Birth: | | 30-03-1990 | | |
| AICTE – ID: | 1-2323653339 | | | |
| Education | <ul style="list-style-type: none"> M. Tech in ECE with First Class with Distinction from Chirala Engineering College, Chirala, [2011-2013]. B. Tech in ECE with First Class from Chirala Engineering College, Chirala, [2008-2011]. | | | |
| Experience | Teaching: 8 | Industry: | Total: 8 | |
| | Research: | Others: | | |
| Area of Specialization | | VLSI & ES | | |
| Course Work: | Digital Electronics, Embedded Systems, VLSI,VHDL,MPMC | | | |
| Details of Ph.D. Scholars /Masters Level Projects :- | | | | |
| | | Completed | Ongoing | |
| Ph.D. guided | | - | - | |
| Master Projects | | - | - | |
| Details of Research Publication :- | | | | |
| | | International | National | |
| Research Publications | | - | 4 | |
| Research Presentations | | - | - | |
| Details of Sponsored Projects/Consultancy :- | | | | |
| | Completed | Ongoing | Amount to be earned | |
| Sponsored Projects | - | - | - | |
| Consultancy Works | - | - | - | |
| No of Books Published :- | | | | |
| No of Patents (Filed & Granted) :- | | | | |
| No of FDPs & Workshops Attended: 10 & 10 | | | | |