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Dr.K. Swarnasri, Vice-Chair.
Dr.Y.Kishore Babu, Secretary & Treasurer.

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Dr. K. Ramesh, Ph.D., Assoc Professor
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Mr. S. Subrahmanyam, Asst.Prof
Mr. P. Raju, Asst.Prof

Department Vision:

The Department of EEE provides programs of the highest quality to produce globally competent technocrats who can address challenges of the millennium to achieve sustainable socio - economic development.

Department mission:

- M1: To provide quality teaching blended with practical skills.
M2: To prepare the students ethically strong and technologically competent in the field of Electrical and Electronics Engineering.
M3: To motivate the faculty and students in the direction of research and focus to fulfil social needs.

Chief Patrons:

Sri. Muppalaneni Srinivasa Rao
- President
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- Vice-President-1
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- Secretary
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- Joint Secretary
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Dr. N. Rama Devi
- Principal

Convener:

Dr. N. Karthik
HOD-EEE Department.

Co-Conveners:

Dr. G. Sai Goutham
Asst. Prof – EEE Department
Mrs. D. Naga Lakshmi
Asst. Prof - EEE Department
Dr .P. Sampath Kumar
BEC IEEE Student Mentor
Asst. Prof - EEE Department
Dr. B. Vijaya Krishna
BEC IEEE Student Mentor
Asst. Prof - EEE Department

Student Coordinators:

Mr. D. Teja Subhash - 4th year EEE
BEC IEEE Student Ph: 9985002595
Miss. M. Jayasri - 4th year EEE
BEC IEEE Student Ph: 6301402629



Bapatla Engineering College::Bapatla (Autonomous)

A One Week Workshop
On
Electronics Hardware Design
13th- 18th October, 2025



Organized by
Department of Electrical & Electronics
Engineering
Bapatla Engineering College:: Bapatla
(Autonomous)
(Affiliated to Acharya Nagarjuna University)
Bapatla-522102, Bapatla Dist.A.P.
Phone:08643-224244,224266,225234
Fax:08643224246
E-mail:bec_principal@yahoo.com
Website: www.becbapatla.ac.in

About BEC Bapatla:

The Bapatla Engineering College (Autonomous), one of the seven educational institutions sponsored by the Bapatla Education Society, was established in 1981 with a vision to impart quality technical education and is affiliated to Acharya Nagarjuna University. The College is a little away from the din and bustle of Bapatla, a town with a historic and hoary past, about 75 Km. south of Vijayawada on Chennai-Vijayawada rail route. The college offers B.Tech. Programs in 9 branches of Engineering- Civil, Computer Science, Mechanical, Electronics and Communications, Electrical and Electronics which are 4 times NBA accredited. Information Technology which is accredited twice, CSE-Data Science, CSE-Cyber Security & CSE-AIML Engineering which is going to be accredited. The college also offers 5 Post Graduate programs in Communication Engineering & Signal Processing, Computer Science Engineering, Structural Engineering, Power Systems Engineering and CAD/CAM. The institute has IETE, IEEE & CSI Student chapters.

About the Department:

This Department was established in 1995 and now offering Diploma (DEEE) with an intake of 30, B.Tech (EEE) with an intake of 120 and M.Tech (Power Systems engineering) with an intake of 6. The department has Electrical Machines Lab, Computer Simulation Lab, Power Electronics Lab, Electrical Measurements Lab, Control Systems Lab, MPMC Lab, Electrical Workshop Lab, Power Systems Lab, IOT Lab; Electronics design lab & Auto Electronics Lab with state of art equipment. The major equipment includes Power Network Analyzer, DSP Based Drives, Cable Fault Locator, Thermal Image Camera, Numerical Relay Test Kit and Lab Volt Electromechanical Training kit. The Professional Software's available include PSCAD, ETAP, MATLAB, PSIM, NIELVS-II and MIPOWER. The students of the department have bagged 5 times University First Ranks, all India 4th, 11th and 12th Ranks etc.in GATE exam so far. EEE Department handled IEEE Student chapter activities & EEE students actively participated in IEEE events.

About the Program:

This workshop aims to provide participants with an creating physical electronic systems and devices, from concept to production. It encompasses the planning, schematic creation, simulation, PCB layout, prototyping, testing, and final deployment of electronic circuits.

Objective of the Program:

- To design hardware that meets the desired functional requirements of the system (e.g., signal processing, control, communication).
- To ensure the circuit or device operates consistently under different environmental and operating conditions.
- To meet industry standards and safety regulations (e.g., EMI/EMC compliance, thermal safety).
- To design hardware that is easy to test, debug, and repair.

Benefits to Participants:

- Custom-designed hardware ensures faster processing, better accuracy and optimized functionality for specific applications.
- Efficient hardware designs help reduce power consumption and heat generation, leading to longer device life and lower operational costs.
- Modern design techniques allow miniaturization, enabling portable and space-saving electronic products.
- Hardware design enables the creation of innovative, application-specific solutions tailored to user needs.

Resource Person:

Mr. S.Kedarnath
Design Engineer
Bhumitra Technologies, Tirupati



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A One Week Workshop On

Electronics Hardware Design 13th- 18th October, 2025

REGISTRATION FEE:

UG/PG –Students - Rs 300/-

Industry/Faculty / Research Scholar - Rs 500/-

Note: Spot Registration & Offline Payment Only.

Eligibility:

Faculty members from AICTE approved Engineering Colleges, working professionals, practicing Engineers from various Research Organizations, Industries, Postgraduate Students, Under Graduate students of Electrical & Electronics Stream and Research Scholars are eligible to apply. Targeted audience - 4th Year B.Tech EEE Students, Industrial & Resource Persons.