


FACULTY PROFILE

Name of the Faculty:	V K KARTHEEK LINGAMGUNTLA			
Designation:	AssistantProfessor			
Department:	ECE			
Date of Birth:	26-02-1989			
AICTE – ID:				
Education	<p>[PG] in [VLSI DESIGN] with [first division] from [JNTU], [Hyderabad], [From 2010 – To 2012] with 82%.</p> <p>[UG] in [ECE]with [first Division] from [JNTU], [ANANTHAPUR], [From 2005 – To 2009] with 76.8%.</p>			
Experience	Teaching: [9]	Industry: [4]	Total: [Year]	
	Research: [Years]	Others: [Years]		
Area of Specialization	VLSI DESIGN			
Course Work:	UG			
Details of Ph.D. Scholars /Masters Level Projects :-				
		Completed	Ongoing	
	Ph.D. guided	-	-	
	Master Projects	-	-	
Details of ResearchPublication :-				
		International	National	
	Research Publications	-	2	
	Research Presentations	-	2	
Details of Sponsored Projects/Consultancy :-				
		Completed	Ongoing	Amount to be earned
	Sponsored Projects	-	-	-
	Consultancy Works	-	-	-
No of Books Published : NO				
No of Patents (Filed & Granted) : NO				