IEEE Guntur Subsection Advisory Committee:

Dr. Sreenivasa Reddy Edara, Chair.

Dr. N. Usha Rani, Vice-Chair.

Dr. K. Swarnasri, Secretary.

Dr. Y. Kishore Babu, Treasurer.

Advisory Committee:

Dr. G.Ravi kumar, Ph.D., Professor

Dr. K. Ramesh, Ph.D., Assoc Professor

Dr. Ch. Sridhar, Ph.D., Assoc Professor

Organizing Committee:

Dr. K. Kamala devi, Ph.D., Asst. Prof

Dr. Ch.Hari Prasad , Ph.D., Asst. Prof

Dr. J. Pardasaradhi, Ph.D., Asst. Prof

Dr. J.Ravindra, Ph.D., Asst. Prof

Dr. M. S. Dinesh, Ph.D, Asst. Prof

Mr. G. Rajesh, Asst. Prof

Mr. G.Anil Kumar, Asst. Prof

Mr. N. Balakrishna , Asst. Prof

Dr. K.Rajendra, Ph.D., Asst. Prof

Mr. M. Suresh Babu, Asst. Prof

Mrs. D. Nagalakshmi, Asst. Prof

Mr. M. Sivarama Krishna, Asst. Prof

Mr. G. Sai Goutham, Asst. Prof

Mr. P. Karimulla, Asst. Prof

Mr. S. Subrahmanyam, Asst. Prof

Mr. P. Raju, Asst. Prof

Department Vision:

The Department of EEE provides programs of the highest quality to produce globally competent technocrats who can address challenges of the millennium to achieve sustainable socio economic development.

Department mission:

M1: To provide quality teaching blended with practical skills.

M2: To prepare the students ethically strong and technologically competent in the field of Electrical and Electronics Engineering.

M3: To motivate the faculty and students in the direction of research and focus to fulfil social needs.

Chief Patrons:

Sri. Muppalaneni Srinivasa Rao

- President

Sri. Doppalapudi Rama Mohan Rao

- Vice-President-1

Sri. Gelli Dileep Kumar

- Vice-President-2

Sri. Manam Nageswara Rao

- Secretary

Sri. Kommineni Hari Padma Prasad

- Joint Secretary

Sri. Thalluri Ramakrishna Rao

- Treasurer

Patron:

Dr.Nazeer Shaik

- Principal

Convener:

Dr. N.Rama Devi

- Professor & HOD BEC IEEE Student Counsellor EEE Department.

Co-Conveners:

Dr. N. Karthik

Assoc. Prof - EEE Department

Mrs. M. Nagendra

Asst.Prof - EEE Department

Dr. P. Sampath Kumar

BEC IEEE Student Mentor Asst.Prof - EEE Department

Dr. B. Vijaya Krishna

BEC IEEE Student Mentor Asst.Prof - EEE Department

Student Coordinators:

T. S. S. Mallika - 3rd year EEE BEC IEEE Student Chair, Ph: 9966374312

S. T. Murthy Babu - 3rd year EEE

BEC IEEE Student Vice Chair, Ph: 9652911782

M. Bheemeswara reddy - 2nd year EEE

Ph: 7995762843

M. Jayasri - 2nd year EEE

BEC IEEE Student Ph: 6301402629









Bapatla Engineering College::Bapatla (Autonomous)

A Three day workshop on VLSI Design Flow & hands on experience with EDA tools

28 th - 30th October, 2024



Organized by

Department of Electrical & Electronics Engineering

Bapatla Engineering College :: Bapatla

(Autonomous)

(Affiliated to Acharya Nagarjuna University)

Bapatla-522102, Bapatla Dist.A.P.

Phone:08643-224244,224266,225234

Fax:08643224246

E-mail: bec_principal@yahoo.com

Website: www.becbapatla.ac.in

About BEC Bapatla:

The Bapatla Engineering College (Autonomous), one of the seven educational institutions sponsored by the Bapatla Education Society, was established in 1981 with a vision to impart quality technical education and is affiliated to Acharya Nagarjuna University. The College is a little away from the din and bustle of Bapatla, a town with a historic and hoary past, about 75 Km. south of Vijayawada on Chennai-Vijayawada rail route. The college offers B.Tech. Programs in 9 branches of Engineering- Civil, Computer Science & Mechanical Engineering, and Electrical & Electronics Engineering which are 4 times NBA accredited Electronics and Communications which are thrice NBA accredited Information Technology which is accredited once, Data Science, and Cyber Security& AIML Engineering which are going to be accredited. The college also offers 5 Post Graduate programs in Communication Engineering & Signal Processing, Computer Science, Structural Engineering, Power Systems and CAD/CAM. The institute has IETE, IEEE & CSI Student chapters.

About the Department:

This Department was established in 1995 and now offering Diploma (DEEE) with an intake of 30, B.Tech (EEE) with an intake of 120 and M.Tech (Power Systems engineering) with an intake of 6. The department has Electrical Machines Lab, Computer Simulation Lab, Power Electronics Lab, Electrical Measurements Lab, Control Systems Lab, Micro processers Lab, Electrical Workshop Lab, Power Systems Lab, Electronic Devices & Circuits Lab, IOT Lab; Electronics design lab & Auto Electronics Lab with state of art equipment. The major equipment includes Power Network Analyzer, DSP Based Drives, Cable Fault Locator, Thermal Image Camera, Numerical Relay Test Kit and Lab Volt Electromechanical Training kit. The Professional Software's available include PSCAD, ETAP, MATLAB, PSIM and MIPOWER. The students of the department have bagged 5 times University First Ranks and all India 4th, 11^{th} and 12^{th} etc. in GATE so far. EEE Department handled IEEE Student chapter activities & EEE students actively participated in IEEE events.

About the Program:

In this event, the students & faculty will understand VLSI (Very Large Scale Integration) design flow and semiconductor eco system and overview on verification methodologies and physical design hands on experience with EDA (Electronic Design Automation) tools. This workshop is designed in such a way, every student & faculty would actively participate and gain awareness on the career opportunities in the field VLSI.

Objective of the Program:

This workshop aims to equip students and faculty with a thorough understanding of the VLSI (Very Large Scale Integration) design flow and the semiconductor ecosystem. Participants will gain insights into verification methodologies and engage in hands-on experience with Electronic Design Automation (EDA) tools. The event is structured to encourage active participation, fostering a collaborative learning environment. Additionally, it will highlight various career opportunities within the VLSI industry, inspiring attendees to explore potential pathways in this dynamic field.

During this training program participants will:

- ✓ Gain a foundational understanding of SoC design
- ✓ Explore various tool & project specification
- ✓ Receive an overview of verification methodology
- ✓ Implement code for their project

Don't miss this opportunity to:

- * Learn ASIC (Application Specific Integrated Circuit) Design flow- frontend and backend.
- * Get hands-on experience through practical exercises.
- * Network with industry professionals.
- * Advance your career in VLSI.

Resource Person:

Mr Basavaraj H

Senior Faculty
Maven Silicon VLSI & Embedded
Systems Training Center, Bangalore.











Bapatla Engineering College::Bapatla (Autonomous)

A Three day workshop on VLSI Design Flow& hands on experience with EDA tools

28 th - 30th October, 2024

REGISTRATION LINK:

https://forms.gle/zMPQkfiptL4cJdeJ8

REGISTRATION FEE:

Non- IEEE Membership students - Rs 200/-

IEEE Membership students - Rs 100

Note: 1. Payment Only OFFLINE
2. Accommodation on your own basis.

Eligibility:

Faculty members from AICTE approved Engineering Colleges, working professionals and practicing Engineers from various Research Organizations and Industries, postgraduate Students, Under Graduate students of Electrical & Electronics stream and Research Scholars can also apply.

Important Dates:

Last date for Registration : 14-10-2024 Confirmation through mail : 15-10-2024

PLATFORM: OFFLINE